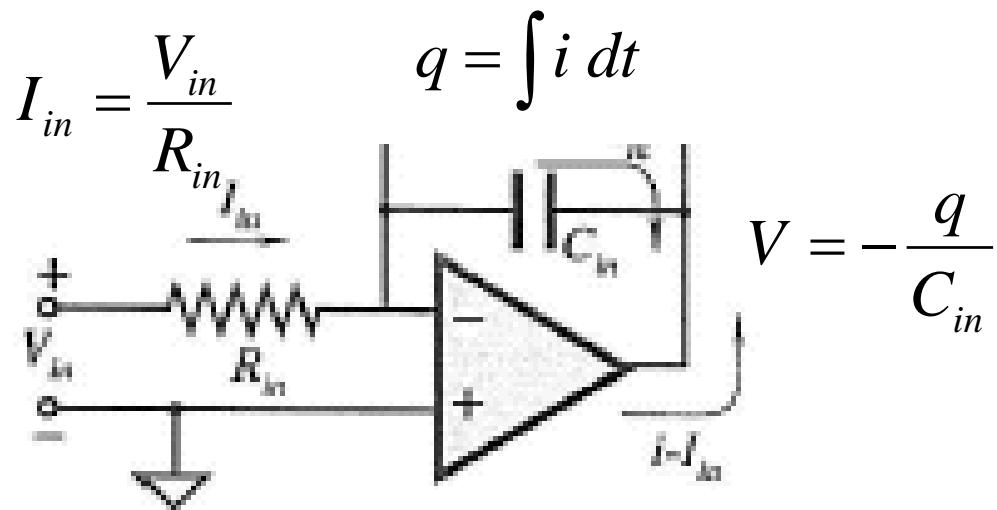


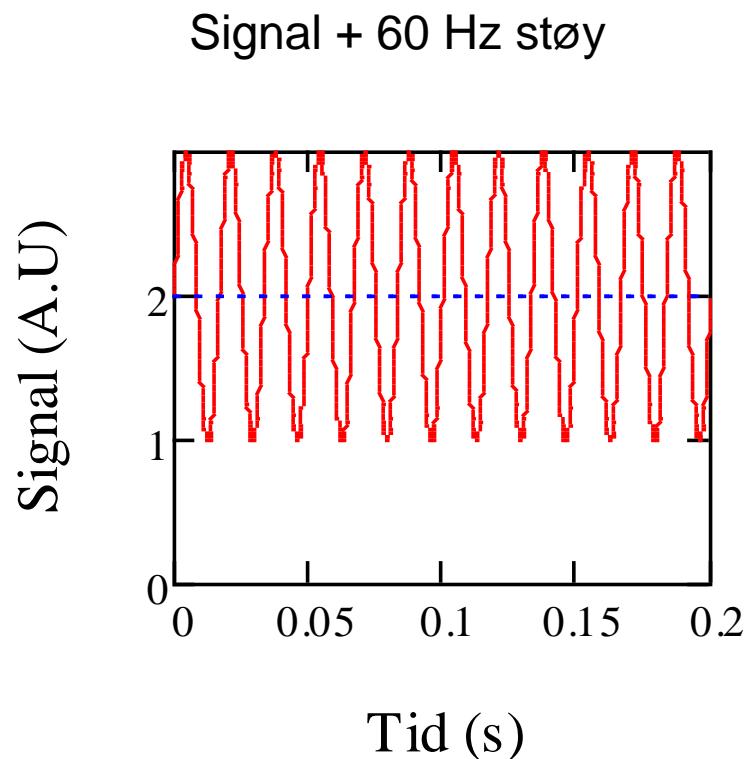
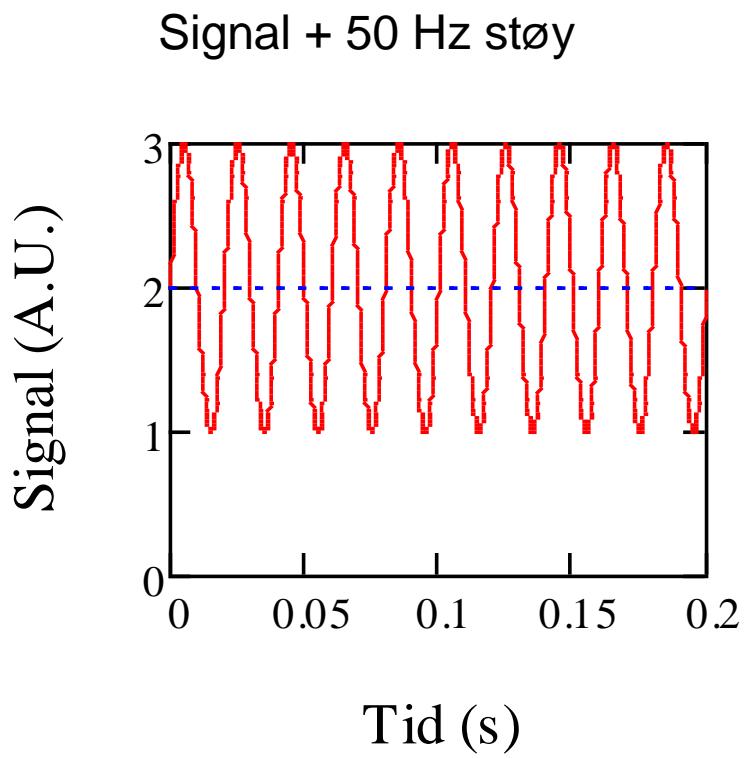
Integrator



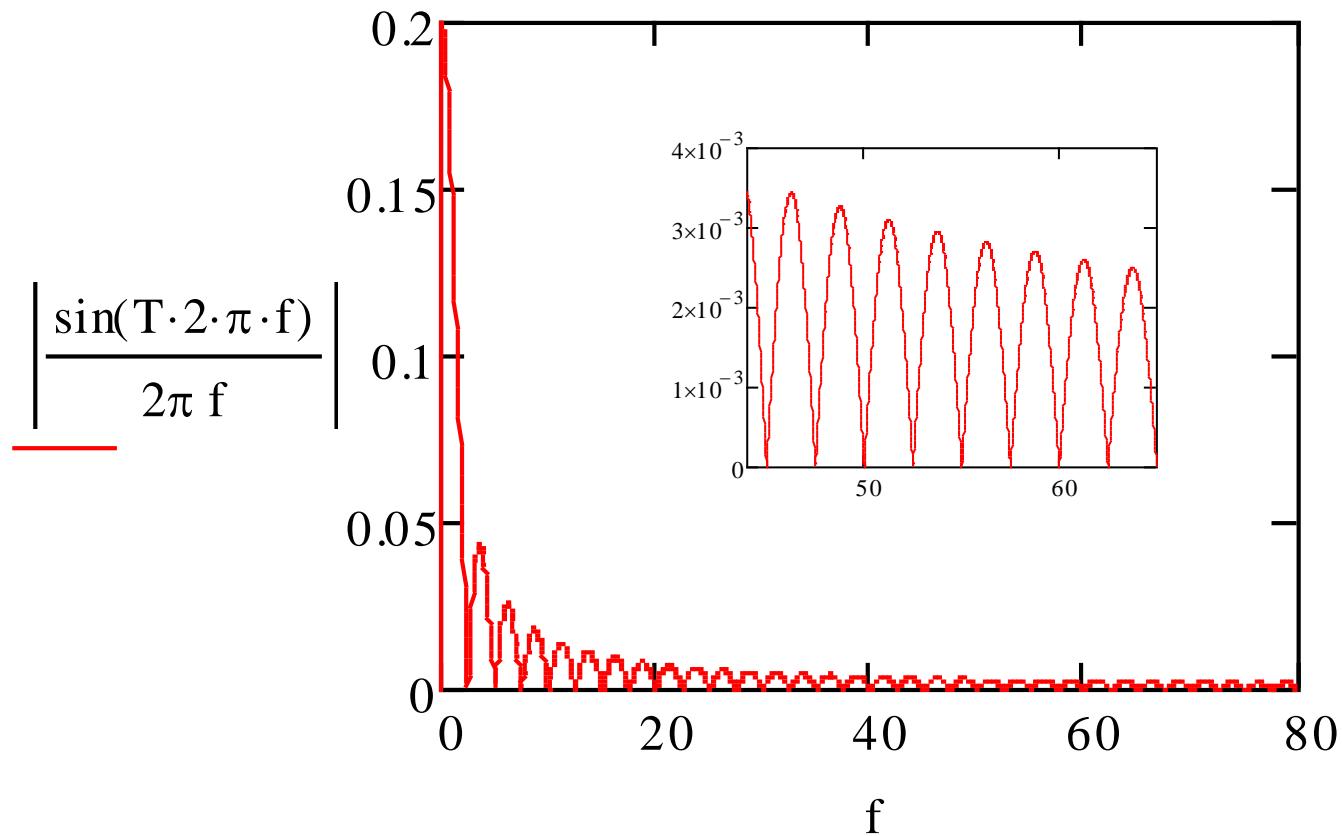
$$V_{out} = \int \frac{V_{in}}{R_{in} C_{in}} \, dt$$

Paynter fig 22.34, Fraden sec 5.4

Integrasjon og støy



Filteregnskaper



Dual-slope A/D converter

182

5 Interface Electronic Circuits

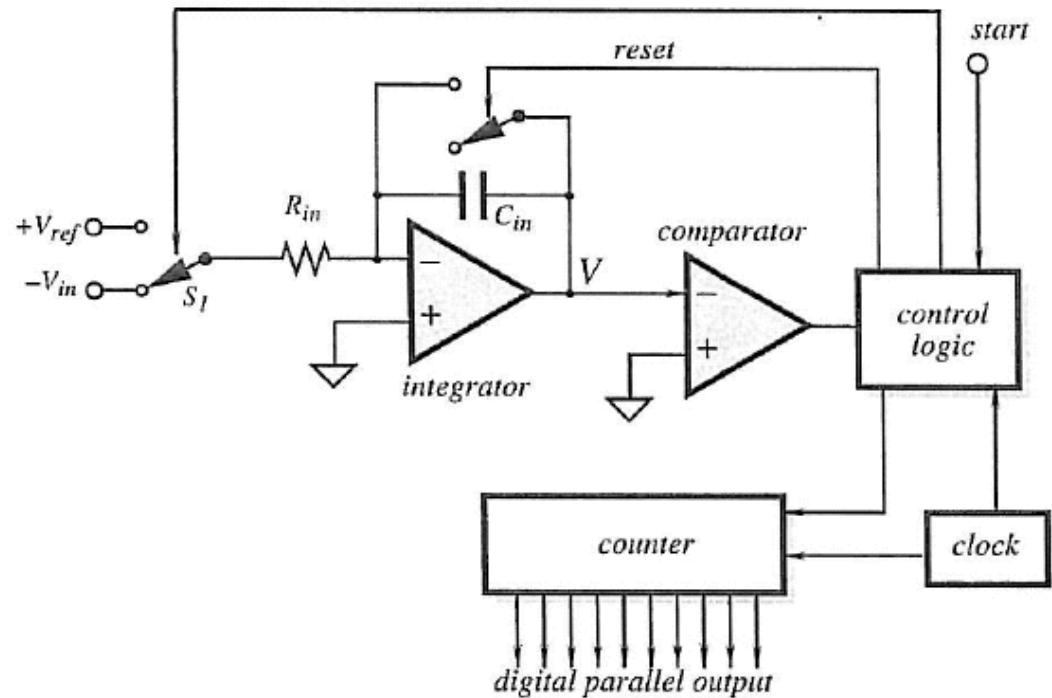


Fig. 5.27. Dual-slope A/D converter.

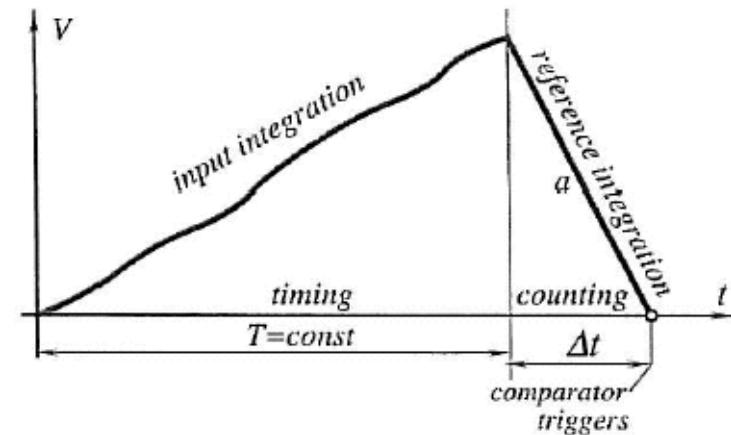


Fig. 5.28. Integrator output in a dual-slope A/D converter.

Spennings til frekvensomformer

180 5 Interface Electronic Circuits

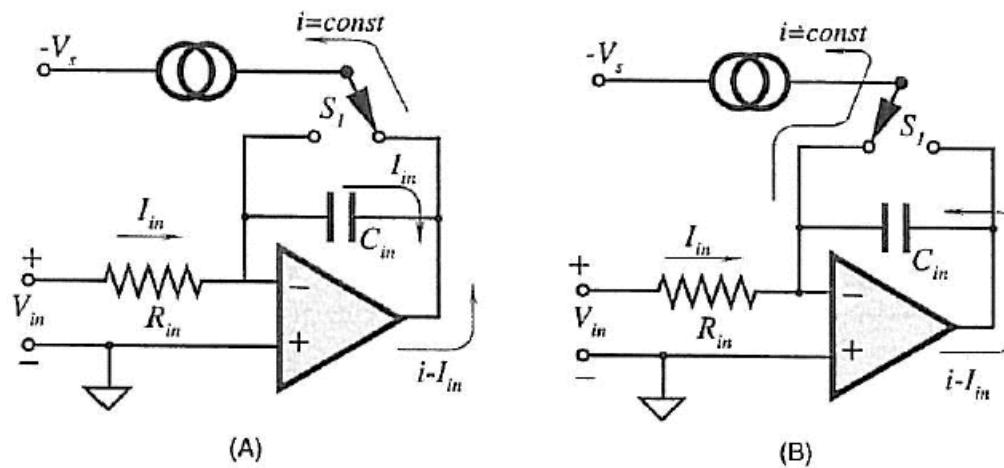
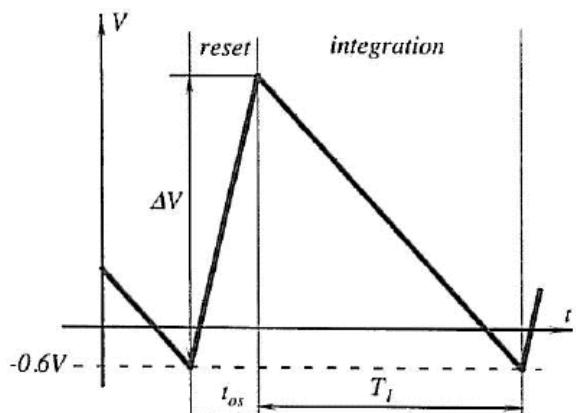


Fig. 5.25. Integrate and deintegrate (reset) phases in a charge-balance converter.

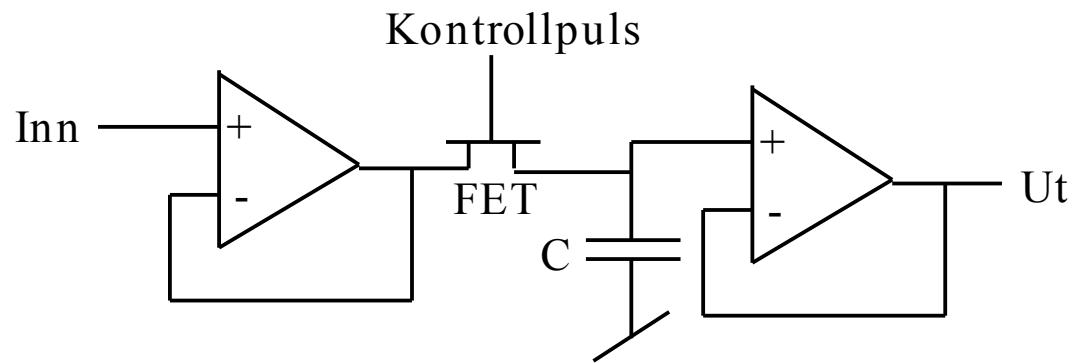


$$f_{\text{out}} = \frac{1}{t_{os} + T_1} = \frac{I_{in}}{t_{os} i} = \frac{V_{in}}{R_{in}} \frac{1}{t_{os} i}.$$

$$f_{\text{out}} = G V_{in}.$$

Fig. 5.26. Integrator output in a charge-balance converter.

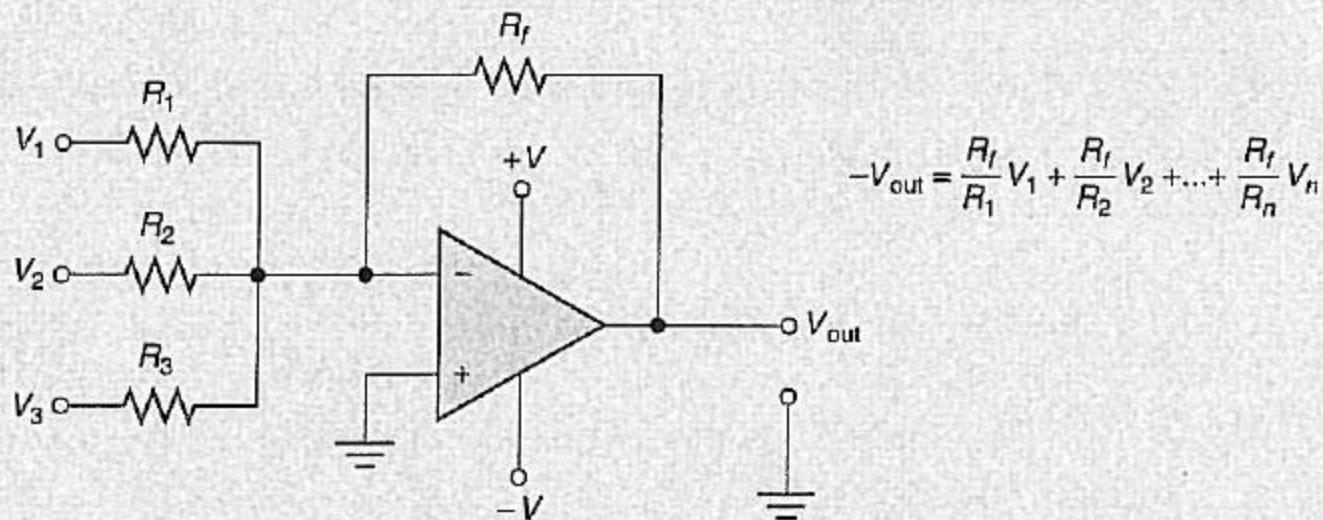
Sample&Hold



Kompendiet

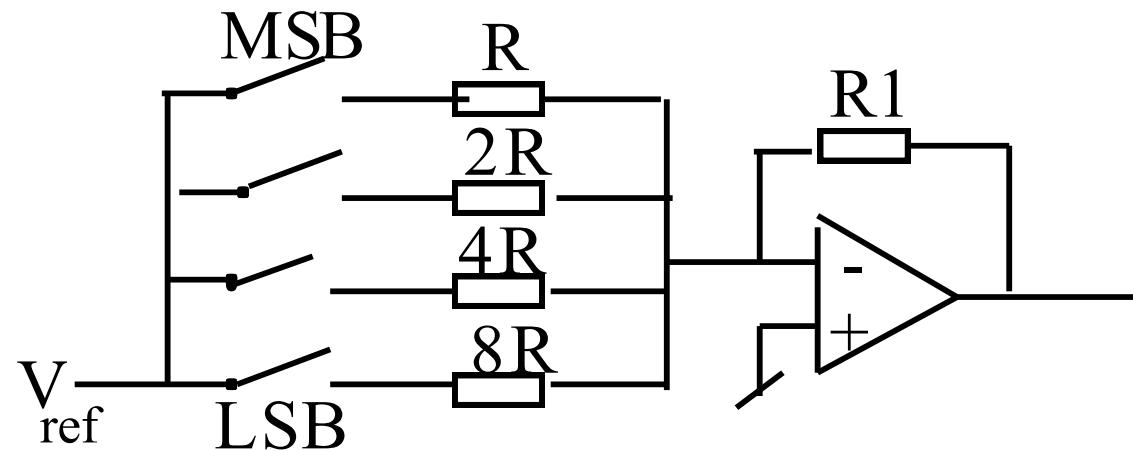
Summasjon

Summing Amplifiers

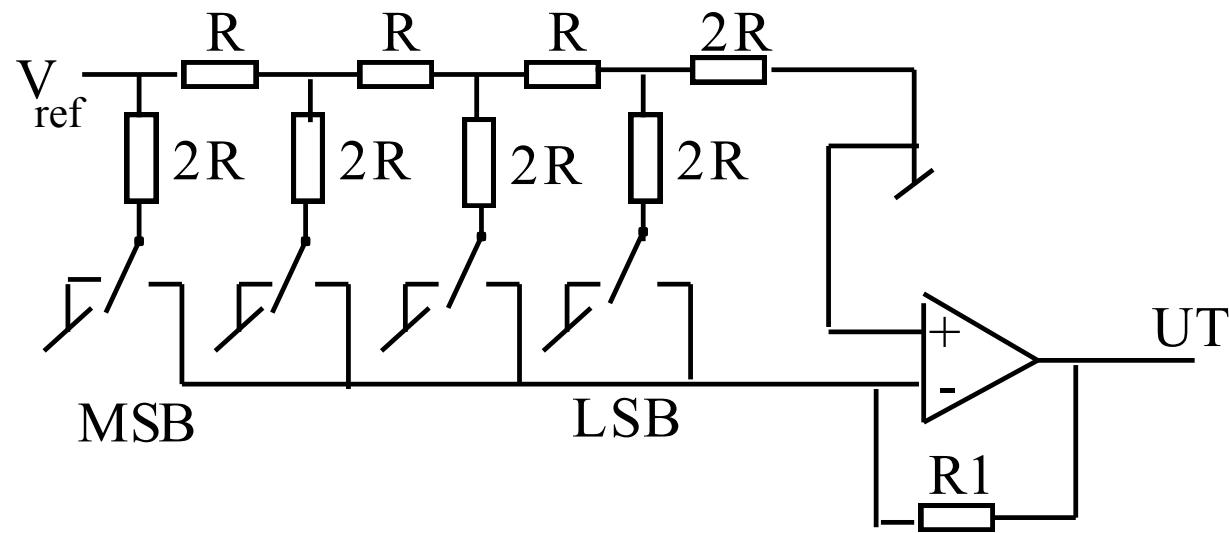


Paynter fig 22.43

Digital til Analog - binært nettverk



Digital til Analog R-2R



Kompendiet

Analog til Digital – Suksessive Approksimasjoner

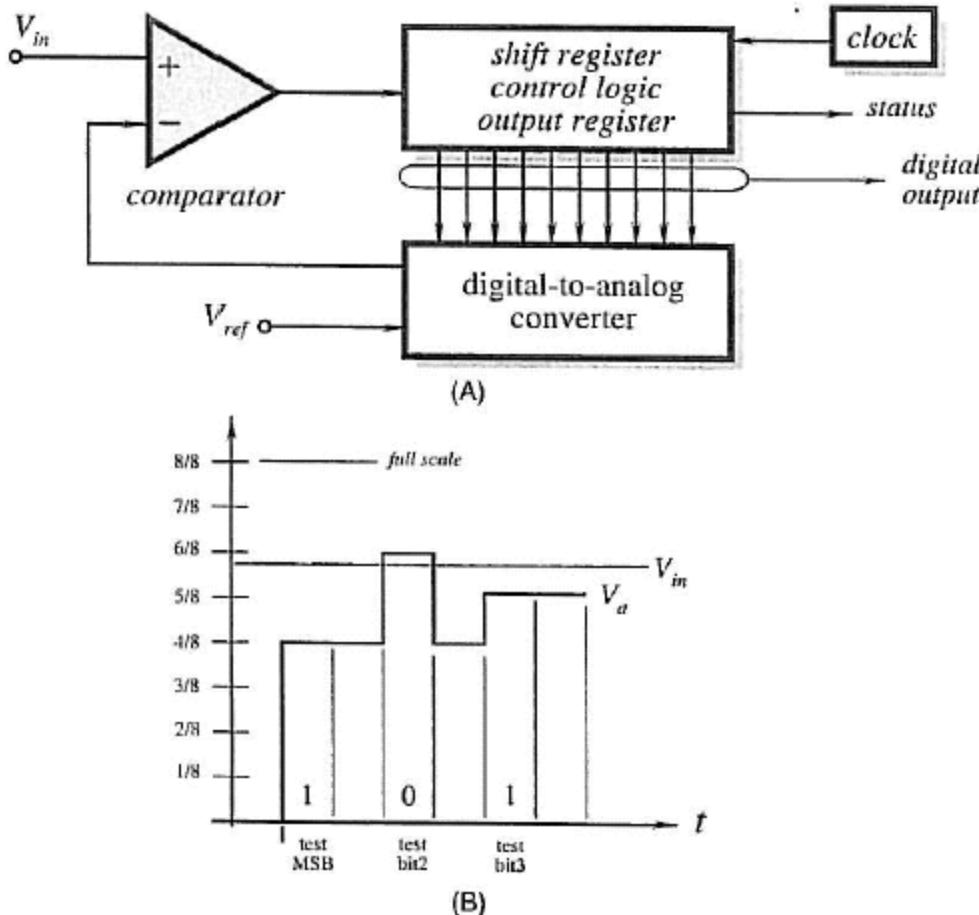
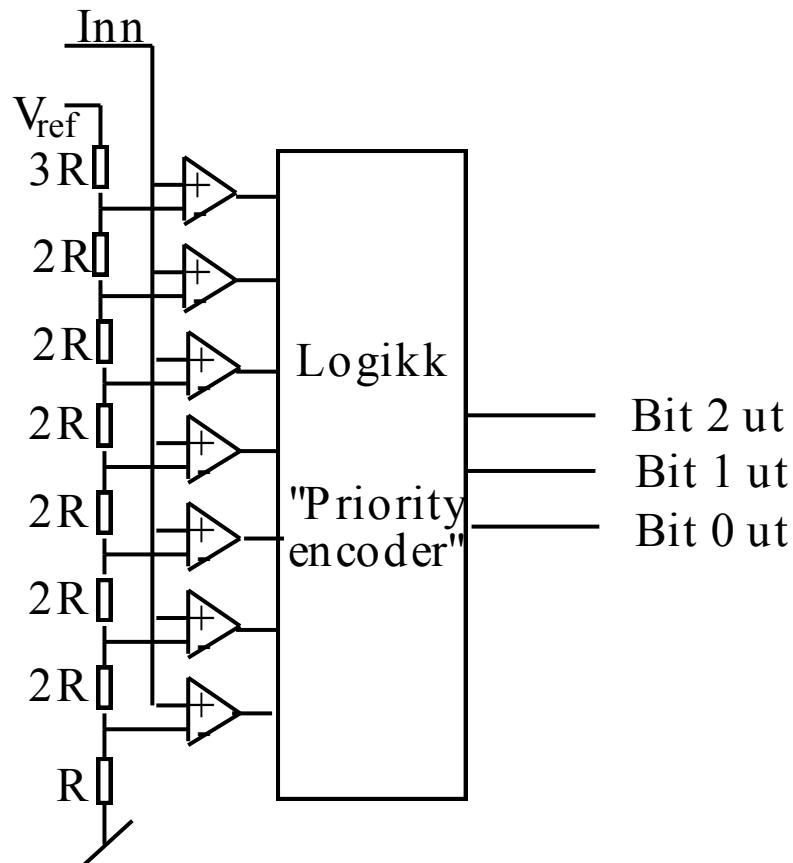
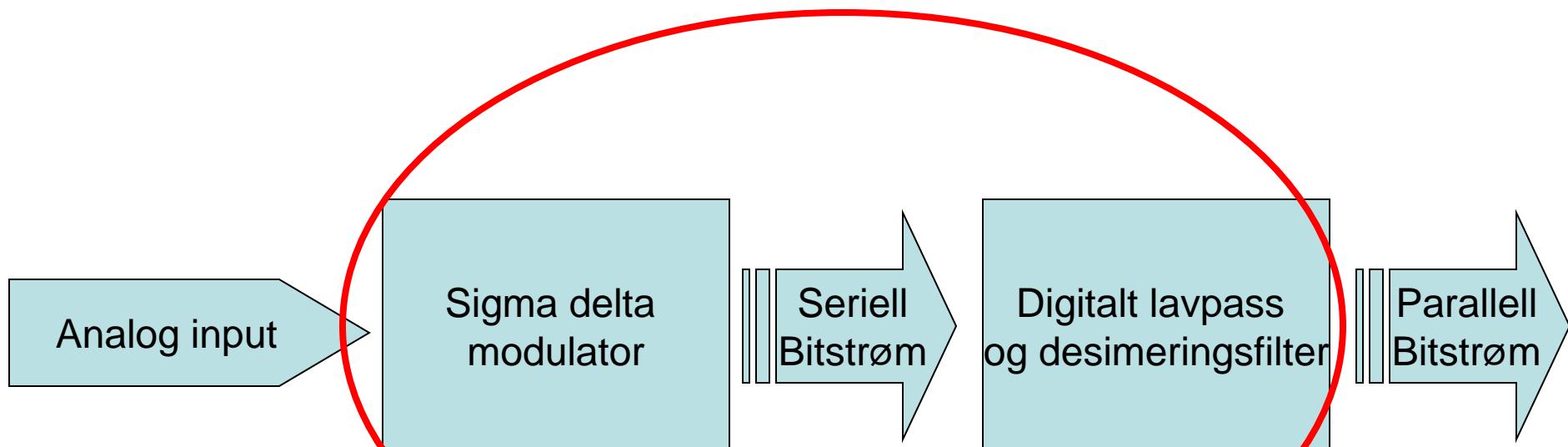


Fig. 5.29. Successive-approximation A/D converter: (A) block diagram; (B) 3-bit weighing.

Analog til Digital – Direkte (Flash)



Sigma delta konverteren



- Digitaliserer et analogt signal
- Består av en sigma delta modulator og et digitalt filter

Hva gjør sigma delta modulatoren?

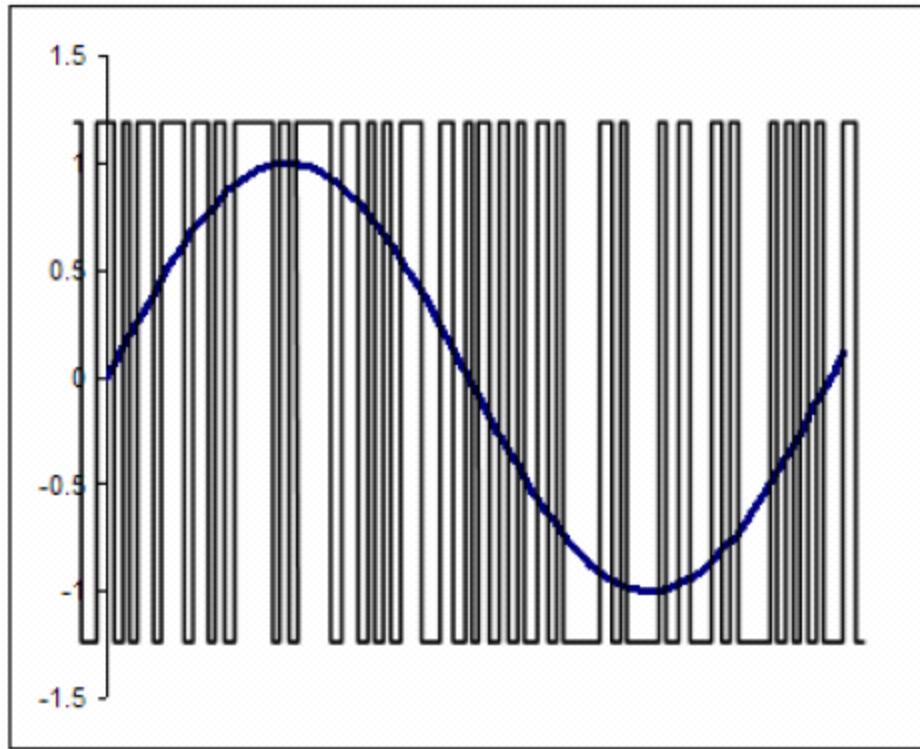


Figure 2 - $\Sigma\Delta$ modulator output representing sine-wave input

Gjør om et analogt signal til et digitalt signal der gjennomsnittet av det digitale signalet representerer det analoge signalet.

Hva gjør desimeringsfilteret

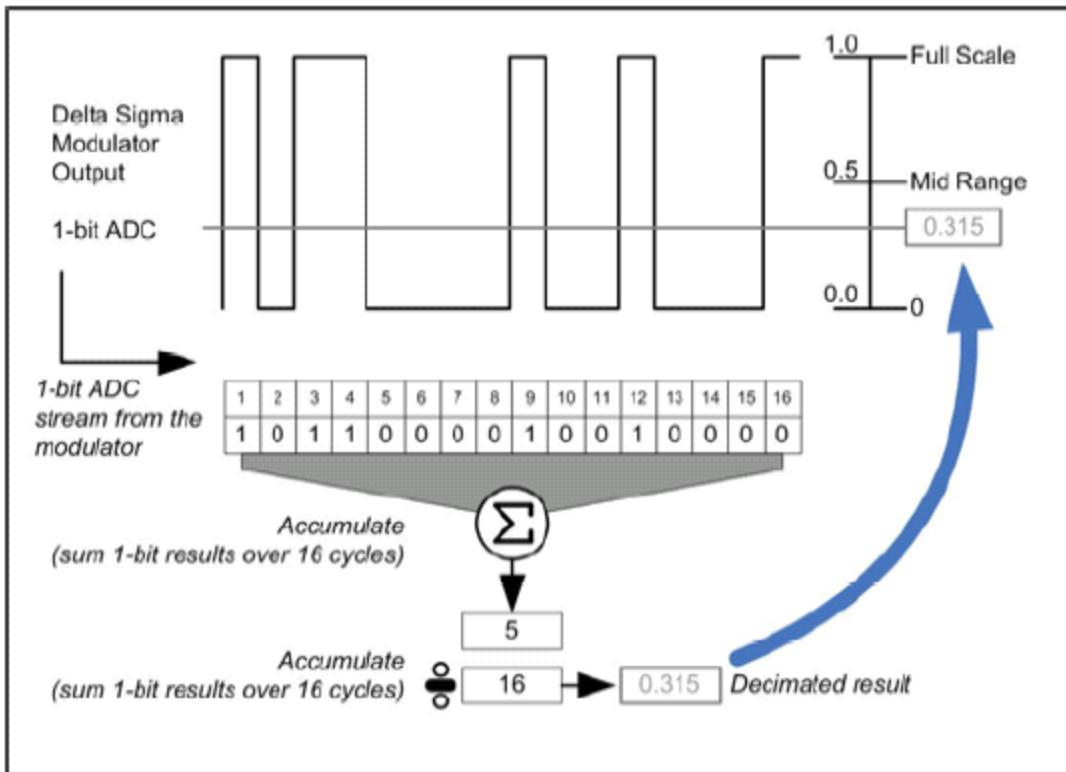
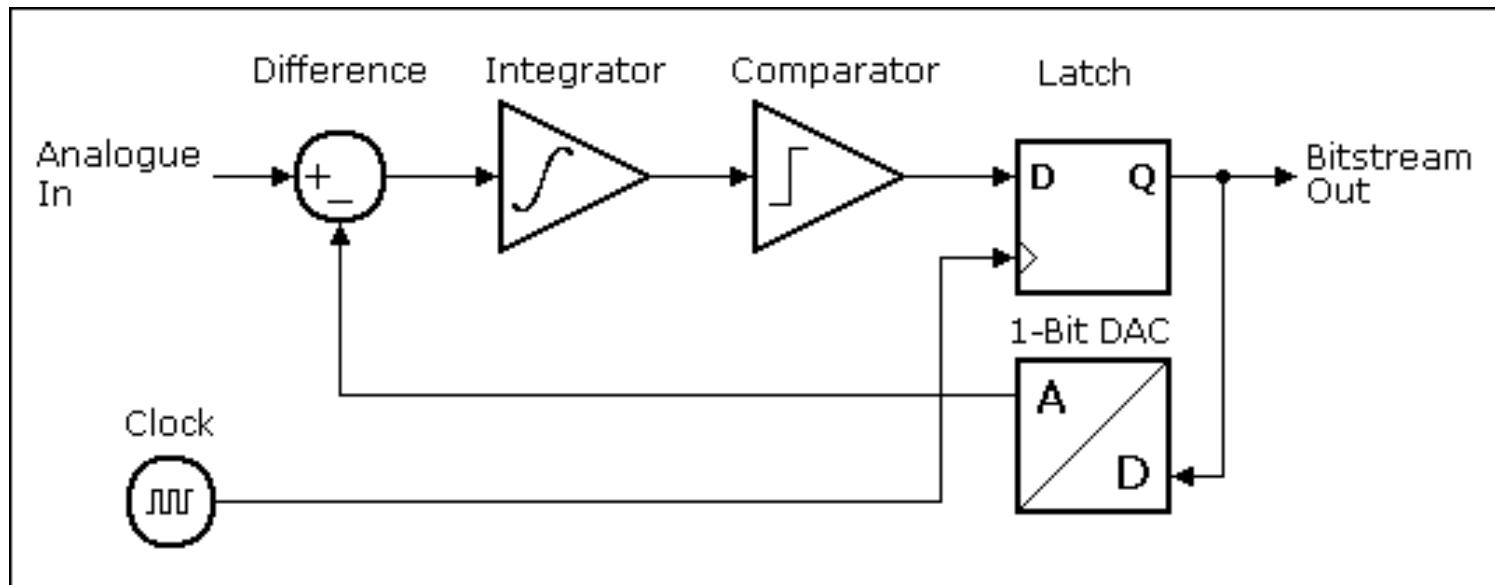
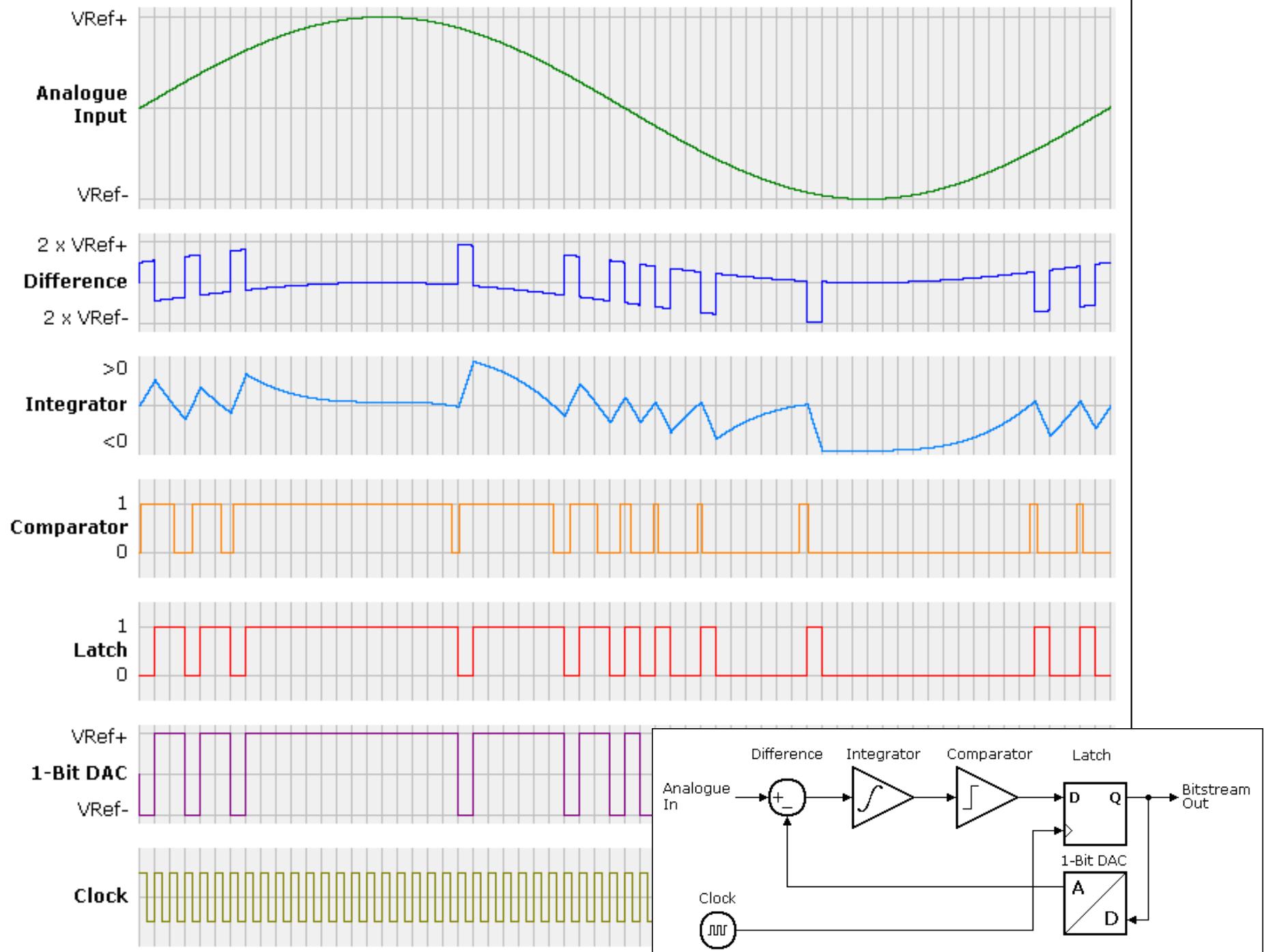


Figure 3 - $\Sigma\Delta$ A/D stream accumulated and decimated to represent an n-bit value of the input

Hvordan ser modulatoren ut?





Bruk av modulatoren alene

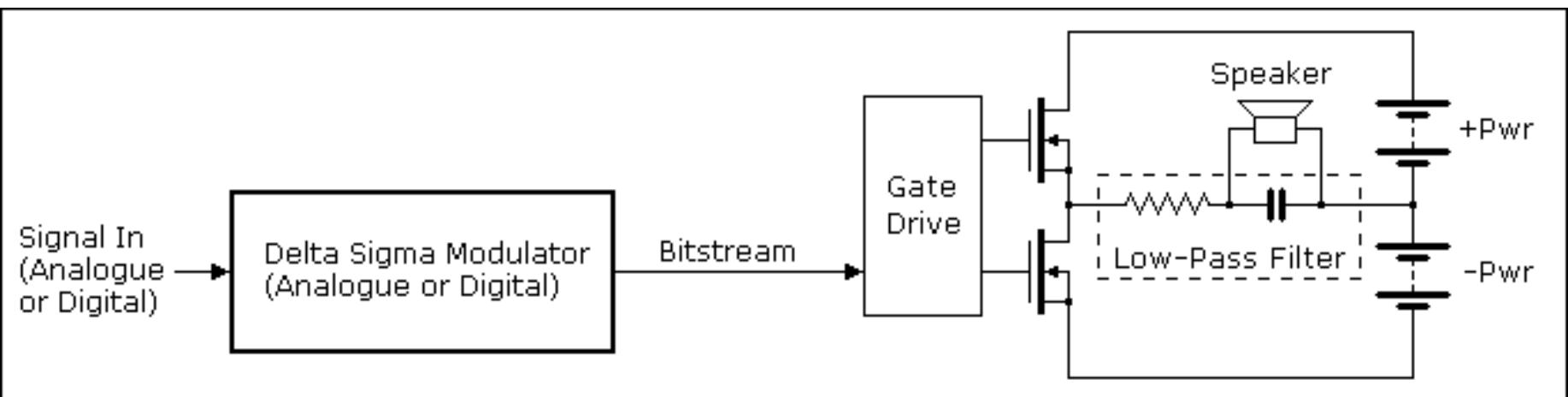
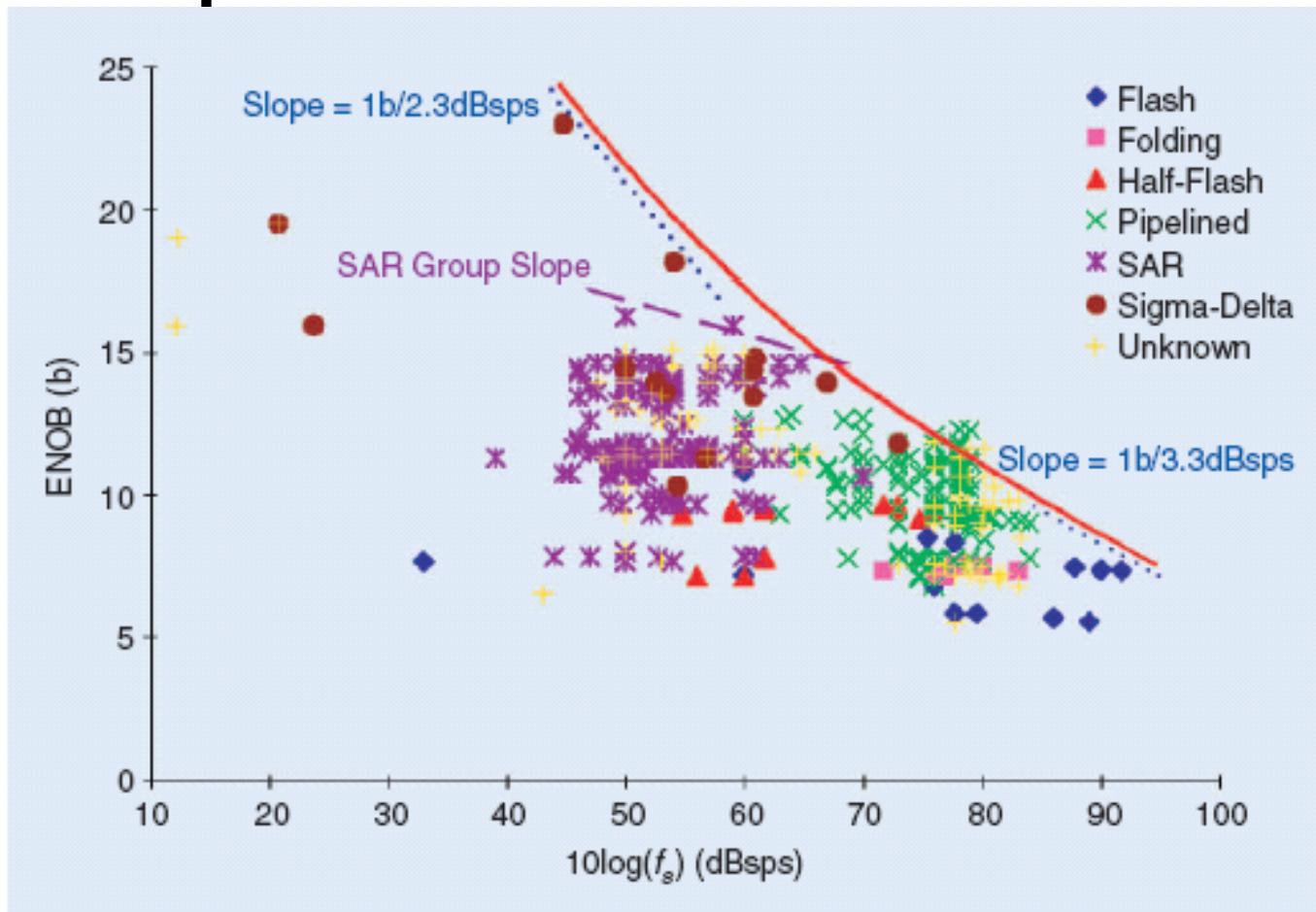


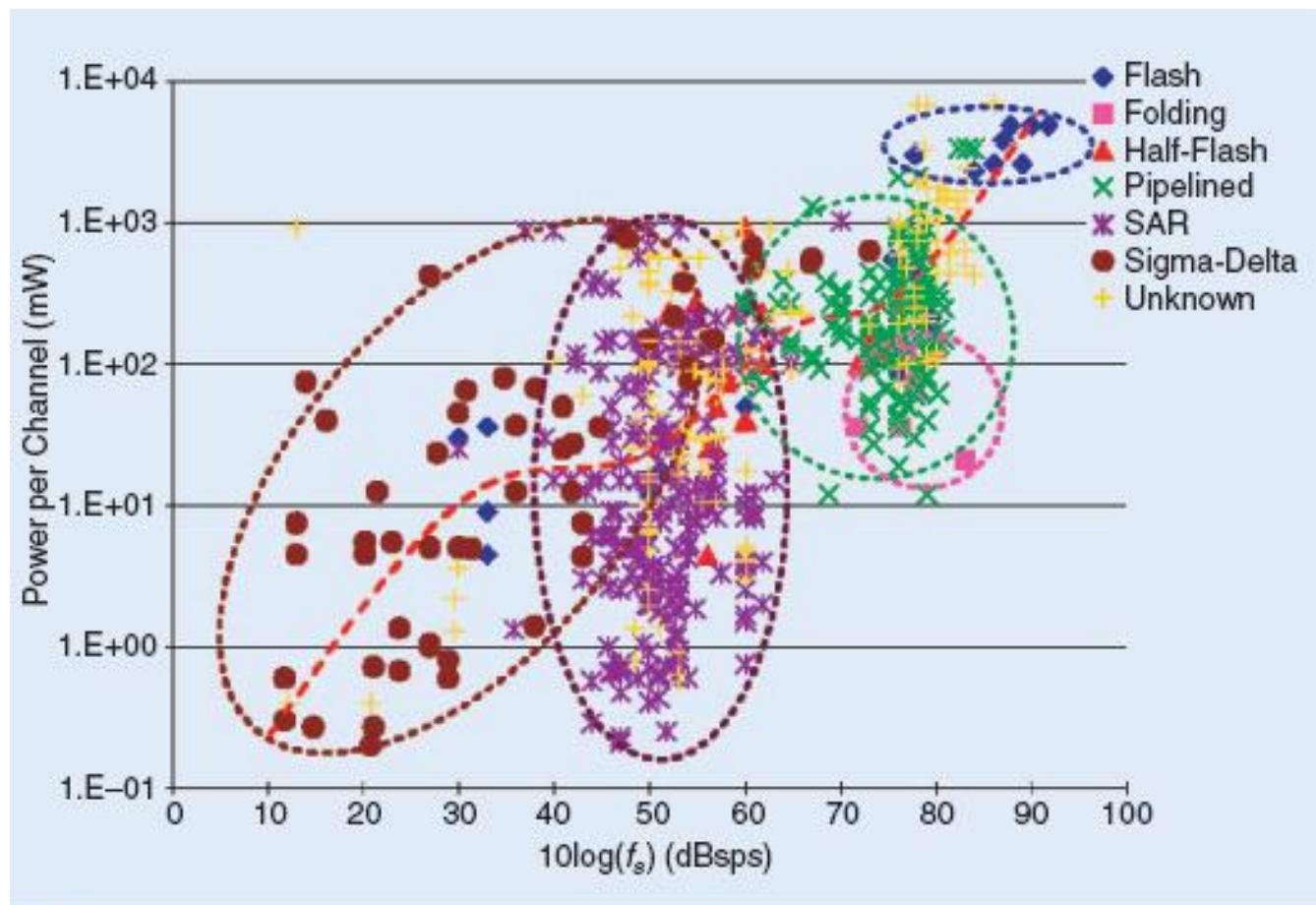
Figure 19 - Power Amplifiers using a Digital Output Stage (D-klasse)

Sample rate vs. effektive bit



[FIG2] ENOB versus sampling rate.

Energiforbruk vs. hastighet



IG4] Power versus sampling rate.