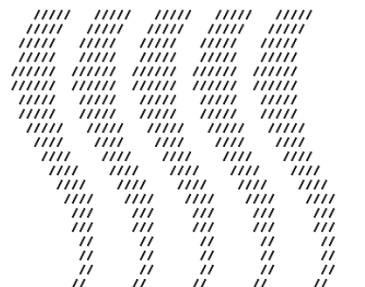




FYS 4220/9220 – 2011 / #6

Real Time and Embedded Data Systems and Computing

# Real-Time Operating systems in particular VxWorks

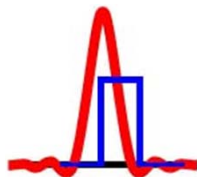


T O R N A D O  
Development System  
Host Based Shell  
Version 2.0.2

Copyright 1995-1999 Wind River Systems, Inc.



PowerMIDAS M5000 (VME)





## What makes an Operating System Real-Time ?

- In previous lectures we have defined some key characteristics for Real-Time processing and discussed mechanisms, such as interprocess communication, that are essential for building Real-Time / embedded systems, without “binding” the functionalities to a particular operating system.
- The next pages are from a recent lecture on RTOS at the School of Informatics at the University of Edinburgh, which summarizes very well requirements for a Real-Time OS.
  - The author, prof. Michael O’Boyle, is director of Institute for Computing Systems

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# Lecture 8

## Real-Time Operating Systems

M O'Boyle

February, 2010



## OS for Embedded Systems

- Embedded Systems, like traditional ones, need OS functions & services to manage h/w & s/w resources of computer
- Two ways of achieving this functionality:
  1. provide it within the programming language (e.g. some Ada implementations code this into the RTSS)
  2. provide it through a separate *Real-time Operating System (RTOS)*
- In both approaches, OS acts as interface between h/w below & application programs (periodic + sporadic processes) above
- Both have arguments in favour of & against

## Requirements of RTOS Predictability and Control

- **Predictability** - most critically w.r.t. *time*
  - all services executed *within* bounded & known times & *at* controlled & known times
  - other resources, such as, files, I/O devices, etc. as well as fault management, should be predictable
- **Visibility & Control** for system components - RTOS user must be able to access & control h/w & system behaviour:
  - necessary to guarantee predictability
  - at the same time, level of abstraction for handling these should be convenient

## Requirements of RTOS - Flexibility

- **Openness** - RTOS should be an *open* system
  - should define a flexible set of mechanisms without forcing a particular policy on a user
  - e.g. should allow choice of different policies for task scheduling



## RTOS Functions & Services

- Functions to access & control both absolute & relative time -
  - many clock-related operations execute at highest priority & not-interruptible
  - OS timers also need to return values of a fine granularity
- Process & Thread management
  - operations to create, initialise, activate, terminate, communicate & synchronise between tasks
  - possible support for periodic & sporadic processes
  - facilities for task scheduling with a specifiable scheduling policy
- Operations for generating & handling s/w interrupts & context switches - to implement *exception handling* services

## RTOS Functions & Services: Time

- Device management utilities must be accessible at the application-RTOS interface:
  - required to control & access sensors, timers & conventional I/O devices
  - e.g. to initiate an I/O operation, read state of a device, defining & connecting interrupt handlers
  - also facilities for attaching new real-time devices
- RTOS cannot be given the responsibility of performing main memory management transparently:
  - for control purposes, users must do it themselves
  - RTOS needs to provide suitable primitives to manage memory



## Synchronisations & Communications in RTOS

- Lowest kernel functions must often be executed atomically or as locked critical sections
- For single-processor systems, indivisibility can be achieved by disabling interrupts during that function execution
- RTOS provide range of synchronisation facilities, such as, locks, semaphores, signals, messages, etc. - predictability of timing behaviour a key feature
- Different types of timeout mechanisms also provided



## Additional wish-list for RTOS's

- The possibility of building user specific RT kernels to match the wide range of processing machines for RT/Embedded, from microcontrollers and soft core processors to large distributed systems
- Quality assured
- An excellent Development Environment
- Debugging and test tools
- RTOS support of that particular computer architecture you want to use
  - See following pages
- Portability, that is, POSIX compliant
- And of course, "at the end of the day", the price is important!

Homepage > Community > Software Enablement > Real-Time Operating Systems (RTOS)

RSS updates Share this

### Quick Links

- Processors
- Multimedia
- Tools
- Find an ARM Partner

### Software Enablement

- > Adobe
- > Google
- > Linux
- > Microsoft
- > **Real-Time Operating Systems (RTOS)**
- > Symbian Foundation
- > UEFI

## Real-Time Operating Systems (RTOS)

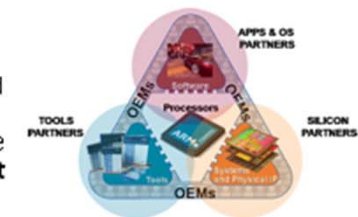
The ARM architecture is supported by all major vendors of Real-Time Operating Systems (RTOS).

Many embedded systems require software to respond to inputs and events within a defined short period of time. Such systems can be categorised as **hard real-time**, where missing a response deadline is unacceptable (for example an anti-lock braking system), and **soft real-time**, where hitting a deadline is desirable but not critical. In both types of system, a degree of **determinism** is important.

RTOSes are designed to control an embedded system, and to deliver the real-time responsiveness and determinism required by the controlled device. Applications run under the control of the RTOS, and their allocated CPU time is scheduled by the RTOS kernel.

In modern systems, a RTOS consists not only of a real-time kernel, but also higher-level functions such as device management (USB, UART, Ethernet, LCD etc), file systems, protocol stacks (CAN, TCP/IP, HTTP etc) and graphical user interfaces (GUI).

See the "RTOS vendors" tab below to see a table of [ARM Connected Community Partners](#) whose RTOSes support the ARM architecture.



(View Larger Real-Time Operating Systems (RTOS) Image)

ARM Advantage

RTOS vendors

### Featured Products

- AMBA Cache Controllers**  
Level-2 Cache Controllers for AHB and AXI processo...
- Mali Software**  
Optimized 3D graphics software that works with or ...
- Logic IP - Standard Cell Libraries, Power Management Kit**  
Optimal processor performance and power efficiency...
- Cortex-A5 Processor**  
ARM's most power efficient, low cost processor ena...

### News & Events

- ARM, Dolby and Ittiam Collaborate To...**  
11 Sep 2010
- ARM Technology Conference**  
The technical prog...

The ARM architecture is supported by all popular RTOS vendors in the embedded market. Below is a table showing the RTOS companies, their product, and an indication of which ARM processor families they currently support:

Company	RTOS	Cortex-			Classic		
		A	R	M	ARM11	ARM9	ARM7
<a href="#">Altreonic</a>	OpenComRTOS			•			
<a href="#">American Megatrends</a>	MegaRAC					•	
<a href="#">CMX Systems</a>	CMX-RTX	•	•	•		•	•
<a href="#">eCosCentric</a>	eCos			•		•	•
<a href="#">eForce</a>	µC3			•			•
<a href="#">ENEAA</a>	OSE			•	•	•	•
<a href="#">eSOL</a>	eT-kernel	•			•	•	•
<a href="#">Express Logic</a>	ThreadX	•	•	•	•	•	•
<a href="#">FreeRTOS.org</a>	FreeRTOS			•		•	•
<a href="#">Green Hills Software</a>	INTEGRITY, VelOSity	•	•	•	•	•	•
<a href="#">Huone Inc</a>	ionESS						
<a href="#">Hycron Electronic</a>	EmbeddedOS				•	•	
<a href="#">IAR Systems</a>	PowerPAC			•	•	•	•
<a href="#">KADAK</a>	AMX RTOS				•	•	•
<a href="#">Keil</a>	Keil RL			•		•	•
<a href="#">LinuxWorks</a>	LynxOS, Blue Cat	•				•	•
<a href="#">Mentor Graphics</a>	Nucleus OS	•	•	•	•	•	•
<a href="#">Micrium</a>	µC/OS-III			•	•	•	•
<a href="#">Micro Digital Inc</a>	SMX RTOS			•		•	•
<a href="#">OpenSynergy GmbH</a>	COQOS					•	
<a href="#">Pengutronix</a>	OSELAS		•	•	•	•	•
<a href="#">Phoenix Technologies</a>	HyperSpace					•	•
<a href="#">QNX Software Systems</a>	Neutrino	•			•	•	
<a href="#">Quadros Systems</a>	RTXC	•	•	•	•	•	•
<a href="#">Quantum Leaps</a>	QP-nano			•		•	•
<a href="#">Radisys</a>	Microware OS-9					•	•
<a href="#">RISC OS Ltd</a>	RISC OS					•	•
<a href="#">Rowebots</a>	Unison v4			•			
<a href="#">rt-labs AB</a>	rt-kernel			•		•	•
<a href="#">SCIOPTA</a>	SCIOPTA	•	•	•	•	•	•
<a href="#">Segger</a>	EmbOS	•	•	•	•	•	•
<a href="#">Semihalf</a>	FreeBSD	•				•	
<a href="#">SYSGO</a>	PikeOS	•				•	
<a href="#">Wind River Systems</a>	VxWorks	•			•	•	•
<a href="#">Wittenstein</a>	OpenRTOS, SafeRTOS			•		•	•



## Real-Time OS for the ARM architecture



# The NIOS II soft core embedded processor

- **Nios II** is a 32-bit embedded-processor architecture designed specifically for the Altera family of FPGAs
- Nios II is comparable to MicroBlaze, a competing softcore CPU for the Xilinx family of FPGA
- Key features:
  - the Nios II architecture is a RISC soft-core architecture which is implemented entirely in the programmable logic and memory blocks of Altera FPGAs. The soft-core nature of the Nios II processor lets the system designer specify and generate a custom Nios II core, tailored for his or her specific application requirements
  - By using custom instructions, the system designers can fine-tune the system hardware to meet performance goals and also the designer can easily handle the instruction as a macro in C/C++
  - Introduced with Quartus 8.0, the optional MMU enables Nios II to run operating systems which require hardware-based paging and protection, such as the Linux kernel. Without an MMU, Nios is restricted to operating systems which use a simplified protection and virtual memory-model: e.g.,  $\mu$ Clinux and FreeRTOS
  - [http://www.altera.com/products/ip/processors/nios2/tools/ni2-development\\_tools.html](http://www.altera.com/products/ip/processors/nios2/tools/ni2-development_tools.html)



# NIOS II embedded Operating system support

## Embedded Operating System Support

Altera's embedded software partners provide an array of operating systems for use with the Nios II processor. Table 1 shows the operating system support available for the Nios II processor.

*Table 1. Embedded Operating System Support for the Nios II Processor*

Operating System	Supplier
<a href="#">eCos</a>	<a href="#">eCosCentric</a>
<a href="#">eCos</a>	<a href="#">Zylin</a>
<a href="#">embOS</a>	<a href="#">Segger</a>
<a href="#">Erika Enterprise</a>	<a href="#">Evidence</a>
<a href="#">Euros RTOS</a>	<a href="#">Euros</a>
<a href="#">Linux</a>	<a href="#">Timesys</a>
<a href="#">Linux</a>	<a href="#">Wind River</a>
<a href="#">Linux</a>	<a href="#">SLS</a>
<a href="#">Linux</a>	<a href="#">CodeSourcery</a>
<a href="#">Linux</a>	<a href="#">Open Source Community</a>
<a href="#">MicroC/OS-II (1)</a>	<a href="#">Micrium</a>
<a href="#">osCAN (2)</a>	<a href="#">Vector</a>
<a href="#">ThreadX</a>	<a href="#">Express Logic</a>
<a href="#">μCLinux</a>	<a href="#">SLS</a>
<a href="#">μCLinux</a>	<a href="#">Open Source Community</a>





# Choice of OS for the Real-Time lab exercises

- Why use VxWorks

- VxWorks from Wind River is an industry leader in Real-Time systems
- It offers a very wide choice of software components (system calls)
- Based on host – target configuration. The development and cross compilation are done on a host computer, typically a PC. The executable code is downloaded to the target processor and linked with the target resident (mini)kernel
- Excellent Development Environment based on the Eclipse Platform.
  - The Eclipse Platform is an open and extensible platform.
- However, VxWorks is a rather expensive solution, however, free as a University program
- Very good tech support in my experience
- For more info on Wind River products, see <http://www.windriver.com/>
- However, nobody is perfect, see next two pages page

- VxWorks Application Programmers Guide

- Selected sections presented in the following
  - The VxWorks-6.2\_Application\_Programmers Guide can be downloaded from the home page



# DeviceLINE

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« [The Smart Meter "Off Switch"](#)  
[New Software Promises iPhone Passwords Without Jailbreak](#) »

## Flaws Uncovered in Popular RTOS

Recently presented at the Security B-Sides and DEFCON conferences in Las Vegas, two critical vulnerabilities have been discovered in VxWorks, Wind River's popular embedded OS that is used in tens of thousands of designs for "smart devices" from organizations including Cisco, Apple and even NASA.



As reported in [SC Magazine](#), one of the vulnerabilities allows hackers to leverage the RTOS's embedded debugging services to take (unauthorized) control of the device.

VxWorks has a service enabled by default that provides read or write access to a device's memory and allows functions to be called.... The vulnerable service, called WDB agent, is a "debugger" for the VxWorks operating system that is used to diagnose problems and ensure code is working properly when a product is being developed. [...]

These two bugs are "just the tip of the iceberg," Moore wrote in a [blog post](#) on Monday, August 2nd, 2010

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- » [Disney to Set Web Toy Standards?](#)
- » [Plane Crash Possibly Linked to Malware](#)
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Android security flaws uncovered

www.linuxfordevices.com/c/a/News/Coverity-report-and-Lookout-Premium-for-Android/

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## Android security flaws uncovered

By Eric Brown  
2010-11-04

Article Rating: ★★★★★ / 1

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**Coverity found 88 "high-risk" defects in Android 2.2's kernel, but noted that Android's defect density is lower than the industry average for mobile OSes. Meanwhile, Lookout Mobile Security announced Lookout Premium for Android, featuring advanced security and privacy features, says eWEEK.**

Software integrity firm Coverity discovered 359 security defects, including 88 high-risk defects, in the source code for the Android 2.2 kernel. Coverity tested only the kernel used in HTC's [Droid Incredible](#) (pictured), sold by Verizon Wireless. However, the tests provide a good indication of the general state of Android security, says the company.

"There are many more vendors than Google and HTC that contributed code into the kernel," stated Coverity co-founder Andy Chou. Coverity did not publicly list specific defects, but informed HTC of the details.

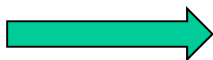
Cited issues in the Android kernel, which is a custom version of the Linux kernel, include problems such as memory corruptions, NULL pointer dereferences, and resource leaks. All these could potentially lead to security vulnerabilities or system crashes, said the company.

The company notes, however, that the Android kernel has a defect density of 0.47 defects per 1,000 lines of code, said to be better than the industry average for similar operating systems of one defect per 1,000 lines of code.

"The Coverity Scan results for the Android kernel we tested show a better than average defect density, meaning this specific kernel is shipping with fewer defects than the industry average for software of this size," stated Chou. "However, a significant number of these defects are the high risk types that our customers typically fix before shipping their products to market."

The finding was released as part of the "2010 Coverity Scan Open Source Integrity Report," which was originally initiated between Coverity and the U.S. Department of Homeland Security in 2006. The study includes analysis of more than 60 million lines of code from 291 widely used open source projects, including Firefox and Apache. All told, some 15,278 defects were found in these open source kernels, says Coverity.

"We are hoping that this report will shed some light on this issue and show that ultimately, for consumers, defects are defects, no matter where the code comes from," Chou said.





# WIND RIVER

VxWorks®

APPLICATION PROGRAMMER'S GUIDE

6.2

Note! This release of VxWorks runs under Workbench Development platform. An earlier release of VxWorks is also installed in the FYS4220 lab, and runs under the Tornado Development platform