

Tentative schedule for FYS4220/9220 (version 2, 18.10.2011)

Week	Date	Subject	Comments	Lab
34	23.08	Intro FPGA		
35	30.08	Intro FPGA + intro VHDL		
36	06.09	intro VHDL		
37	13.09	intro DE2 lab card & design tools ++		Lab1
38	20.09	No lecture	(JKB on travel)	Lab1
39	27.09	No lecture	(JKB on travel)	Lab4 (9220)
40	04.10	No lecture	(JKB on travel)	Lab4 (9220)
41	11.10	VHDL combinational & sync seq logic	TAB (JKB on travel)	
42	18.10	VHDL state machines & large designs		Lab2
43	25.10	No lecture	(JKB on travel)	Lab2
44	01.11	No lecture		Lab3
45	08.11	Digital design & optimization		Lab3
46	15.11	No lecture		
47	22.11	No lecture		Labs Deadline!
48	29.11	FPGA & VHDL summary & questions		

Lab: Wednesdays, 10.15 - 14, Room V442