FYS4260 - Important dates and information

* 30/1 – Deadline for project choice
* 20/2 – Deadline for delivering final schematics
* 27/3 – Deadline for delivering final pcb production files
	+ All production files delivered as a zip file with correct filename, pcb and schematics as separate files.
* 29/4 – Assembly of boards at ELAB
	+ One day 9-17, mandatory!
* 8/5 – Deadline for delivering project report.
* 12/5 – Oral presentation 1015 at LilleFy

Lab days Tuesdays and Thursdays, but the lab is open every day.

You have to present your work to us every week, either Tuesday or Thursday at the lab or by sending in your design by email at the end of the week. This is to make sure you have some progress in your work and for us to be able to correct any mistakes you have made as early as possible.

The assembly day is mandatory, you will not get a second chance to build your board. We start at 0900!

All files delivered must be on the following format: Username-projectname-version

When sending in pcb files, always include the correct schematics as well. Do not zip (except for the last production files).