

Chapter 4 CTL and CTL*

Course "Model checking" Martin Steffen Autumn 2021



Section

Introduction

Chapter 4 "CTL and CTL*" Course "Model checking" Martin Steffen Autumn 2021

Computation tree logic

- CTL: Computation tree logic [2] [3]
- prominent branching time logic
- branching vs. linear time
- remember LTL: models are *paths*, here trees
- we could write

$$s \models \forall \varphi$$
 iff $\pi \models \varphi$ for all path π starting in s



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CTL model checking

(1)

Fixpoints and characterization of *sat*

Explicit vs. symbolic

Symbolic model checking Switching functions Encoding

Unfolding a transition system to a tree



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Computation tree logic

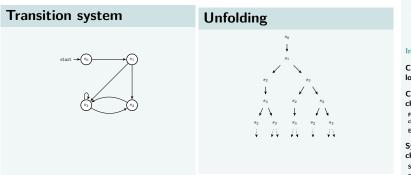
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Linear vs. branching



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specification of a(b+c)parallel systems ab+ac Martin Steffen Introduction start \rightarrow start $\rightarrow C$ Computation tree logic CTL model checking Fixpoints and characterization of sat Explicit vs. symbolic Symbolic model checking Switching functions Encoding

Vending machine example



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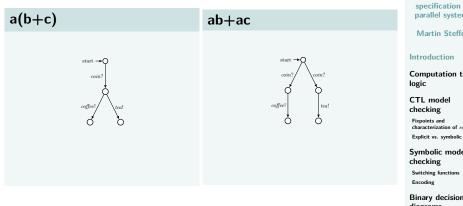
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Computation tree

characterization of sat

Symbolic model





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Syntax



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Example: ∞



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$\forall \Box \forall \Diamond green$

Example: mutex and progress



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$$\forall \Box (\neg crit_1 \lor \neg crit_2)$$

$$(\forall \Box \forall \Diamond crit_1) \land (\forall \Box \forall \Diamond crit_2)$$

Response



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Response



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$\forall \Box (request \rightarrow \forall \Diamond response)$

Restart

LTL?



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$\forall \Box \exists \Diamond start$

LTL?

Derived syntax



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Semantics: |=

 $\begin{array}{c} s \models p \\ s \models \neg \Phi \end{array}$



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 $\begin{array}{ll} \textit{iff} & p \in V(s) \\ \textit{iff} & \mathsf{not} \; s \models \Phi \end{array}$

Semantics for transition systems



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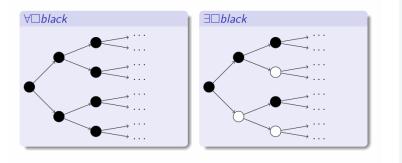
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CTL semantics, example 1





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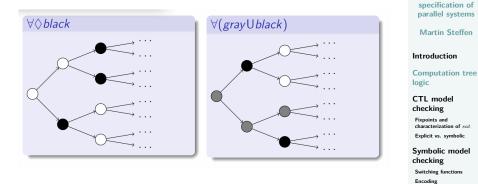
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CTL semantics, example 2





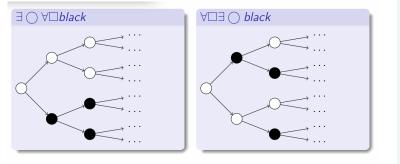
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Binary decision

Working with BDDs

diagrams

CTL semantics, example 3





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Semantics: Sat-set

The satisfaction set $Sat(\Phi)$ is the set of states in a transition system TS that satisfies Φ .

A transition system satisfies Φ , written $TS \models \Phi$, iff all the initial states of the TS satisfies Φ : $I \subseteq Sat(\Phi)$, where I is the set of initial states in TS.



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Comparison with LTL

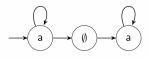
 CTL and LTL are not equally expressive, but neither is more expressive than the other.

Theorem 6.18 [1]

Let Φ be a CTL formula, and φ the LTL formula that is obtained by eliminating all path quantifiers in Φ . Then:

 $\Phi \equiv \varphi$ or there exists no LTL formula that is equivalent to Φ .

Lemma 6.19 $\forall \Diamond \forall \Box a \neq \Diamond \Box a$





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• focus on ENF $(\exists \bigcirc, \exists U, \exists \Box)$

- recursive over structure of formula
- calculate $sat(\Phi)$
- check $I \subseteq sat(\Phi)$
- "global" model checking
- bottom-up traversal of the parse tree of Φ

Normal forms

- state-formulas only
- remember LTL
- positive normal form
- interesting for us: existential NF

ENF



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Model checking CTL

The task is to check whether a transition system TS satisifies a CTL formula Φ . This is the case when all the initial states *I* of the TS satisfy Φ .

Basic Algorithm

 The set Sat(Φ) of all states satisfying Φ is computed recursively ("from inside and out")

2
$$TS \models \Phi$$
 iff $I \subseteq Sat(\Phi)$

This can achieved by a bottom-up traversal of the CTL formula's parse tree.



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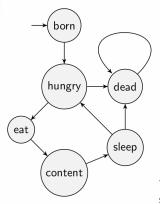
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Model checking: example 1



$$Sat(\forall \Diamond dead) = \{ dead \}$$

 $\forall \Diamond dead?$

The initial state *born* \notin *Sat*($\forall \Diamond dead$), so *TS* $\not\models \forall \Diamond dead$



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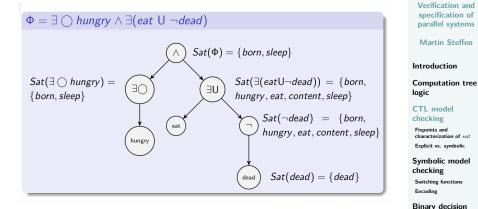
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Model checking: example 2

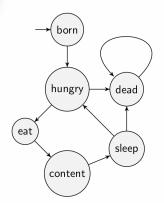


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diagrams Working with BDDs

Model checking: example 2



$$Sat(\Phi) = \{born, sleep\}$$

Because the only initial state is in the formula's satisfaction set, the transition system satisfies the formula.



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Basic algorithm

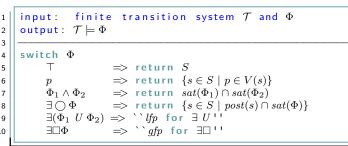


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parallel systems Martin Steffen 1 input: finite transition system ${\cal T}$ and Φ output: $\mathcal{T} \models \Phi$ 2 Introduction 3 for all $i \leq |\Phi|$ do 4 Computation tree logic for all $\Psi \in sub(\Phi)$ with $|\Psi| = i$ do 5 compute $sat(\Psi)$ from $sat(\Psi')$ (* max. genuine $\Psi' \subseteq sat(\Psi)$ *) 6 CTL model checking 7 od Fixpoints and od 8 characterization of sat return $I \subseteq sat(\Phi)$ 9 Explicit vs. symbolic Symbolic model

checking Switching functions Encoding Binary decision diagrams Working with BDDs

Recursive algorithm





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Backward calculation of $\exists \Diamond$



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Encoding

$$\begin{array}{l} T := sat(B); \\ \text{while } pre(T) \setminus T \neq \emptyset \text{ do} \\ \text{let } s \in pre(T) \setminus T ; \\ \\ T := T \cup \{s\}; \\ \text{od}; \\ \text{return } T; \end{array}$$

Massaging the algo

1
$$T := sat(B);$$

2 while $pre(T) \setminus T \neq \emptyset$ do
3 let $s \in pre(T) \setminus T$;
4 $T := T \cup \{s\};$
5 od;
6 return $T;$

loop body

$$T := T \cup \{pick(pre(T))\}$$

Loop condition

 $T \supset T \cup \{pick(pre(T))\} \quad \text{or} \quad T \neq T \cup \{pick(pre(T))\} \ .$



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Massaging the algo

1 T := sat(A);2 while $T \neq T \cup \{pick(pre(T))\}$ 3 $T := T \cup pick(pre(T));$ 4 od; 5 return T;

loop body

$$T := T \cup \{pick(pre(T))\}$$

Loop condition

 $T \supset T \cup \{pick(pre(T))\} \quad \text{or} \quad T \neq T \cup \{pick(pre(T))\} \ .$



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Iteration

exit condition

$$T = T \cup \{pick(pre(T))\}$$
 .

$$\begin{array}{rcl} T_0 &=& A\\ T_{j+1} &=& F(T_j) & \text{where} & F(X) = pick_{\emptyset}(pre(X)) \cup X \end{array}$$

Stabilization

$$A = T_0 \subset T_1 \subset T_2 \subset \ldots \subset T_k = T_{k+1} = T_{k+2} \ldots$$



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Characterization

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Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

Characterization

Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

Goal (a)

$$\begin{array}{lll} T & \supseteq & A \\ T & \supseteq & F(T) \end{array} \quad \text{where} \quad F(X) = pick_{\emptyset}(pre(X)) \cup X \end{array}$$



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Encoding

Characterization

Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

Goal (a)

$$\begin{array}{rcl} T & \supseteq & A \\ T & \supseteq & F(T) \end{array} \quad \text{where} \quad F(X) = pick_{\emptyset}(pre(X)) \cup X \end{array}$$

$$T \supseteq F'(T)$$
 where $F'(X) = pick_{\emptyset}(pre(X)) \cup X \cup A$



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Fixpoint



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Encoding

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$$A = T_0 \subset T_1 \subset T_2 \subset \ldots \subset T_k = T_{k+1} = T_{k+2} \ldots$$

Fixpoint

$$T_{k+1} = F(T_k) = T_k$$

(Pre-)Fixpoint

Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

${\cal T}_k$ solves the following

fixpoint

$$F(X) = X$$



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(Pre-)Fixpoint

Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

${\cal T}_k$ solves the following

| | | checking |
|----------|--------------------|-------------------------------------|
| fixpoint | pre-fixpoint | Fixpoints a characteriza |
| | | Explicit vs. |
| F(X) = X | $F(X) \subseteq X$ | Symbolic checking Switching f |



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That was only half of the characterization



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Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

$$A = T_0 \subset T_1 \subset T_2 \subset \ldots \subset T_k = T_{k+1} = T_{k+2} \ldots$$

That was only half of the characterization

Goal (a)

Find me a set T such that (a) it contains A and such that (b) F(T) does not make it larger.

$$A = T_0 \subset T_1 \subset T_2 \subset \ldots \subset T_k = T_{k+1} = T_{k+2} \ldots$$

Goal (b)

Find the **smallest** set T satisfying **goal (a)**.



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Encoding

Smallest (pre-)fixpoint

• interested in the *smallest* (pre)-fixpoint

| fixpoint | pre-fixpoint |
|----------|--------------------|
| F(X) = X | $F(X) \subseteq X$ |

Facts

1. unique

- 2. smallest fixpoint = smallest pre-fixpoint
- **3.** T_k in the iteration is actually that lfp



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Explicit-state vs. symbolic model checking

The code again:

 $\begin{array}{cccc} 1 & T &:= sat(A); \\ 2 & \text{while} & T \neq T \cup \{pick(pre(T))\} \\ 3 & T &:= T \cup pick(pre(T)); \\ 4 & \text{od}; \\ 5 & \text{return } T; \end{array}$

| Explicit-state | symbolic | ch Ex |
|-------------------------------|----------------------------------|----------------|
| exploring states individually | exploring sets of states (sat) | Sy ch Sv |



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Breadth-first?

```
 \begin{array}{l} 1 \\ 2 \\ 3 \\ 4 \\ 5 \end{array} \left| \begin{array}{l} T := sat(B); \\ \text{while } T \neq T \cup pre(T) \text{ do} \\ T := T \cup pre(T) \\ \text{od}; \\ \text{return } T; \end{array} \right|
```

- likewise: fix-point

But is it actually better?



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Breadth-first?

 $\begin{array}{l} 1 \\ 2 \\ 3 \\ 4 \\ 5 \end{array} | \begin{array}{c} T := sat(B); \\ while \ T \neq T \cup pre(T) \ do \\ T := T \cup pre(T) \\ od; \\ return \ T; \end{array}$

- likewise: fix-point

But is it actually better?

Not really

if the exploration adds pre(T) states individually.



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Breadth-first?

 $\begin{array}{l} 1 \\ 2 \\ 3 \\ 4 \\ 5 \end{array} | \begin{array}{c} T := sat(B); \\ while \ T \neq T \cup pre(T) \ do \\ T := T \cup pre(T) \\ od; \\ return \ T; \end{array}$

- likewise: fix-point

| But | is | it | actually | better? |
|-----|----|----|----------|---------|
| | | | | |

| Not | real | ly |
|-----|------|----|
|-----|------|----|

if the exploration adds pre(T) states individually.

better if one can calculate pre(T) all at once!



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Symbolic model checking

key: efficient representation of

- transition system
- sets of states
- different operations on those sets, in particular
- symbolic exploration by efficent calculation of *pre* in a set of state



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First: characterize all operators

1.
$$sat(\top) = S$$
.

2.
$$sat(p) = \{s \in S \mid p \in V(s), \text{ for any } p \in P\}.$$

3.
$$sat(\Phi_1 \land \Phi_2) = sat(\Phi_1) \cap sat(\Phi_2).$$

4.
$$sat(\neg \Phi) = S \setminus sat(\Phi)$$
.

5.
$$sat(\exists \bigcirc \Phi) = \{s \in S \mid \exists s'.s \to s' \land s' \in sat(\Phi)\}$$

6.
$$sat(\exists (\Phi_1 \ U \ \Phi_2))$$
 is the *smallest* subset T of S such that

6.1
$$sat(\Phi_2) \subseteq T$$
 and

6.2 $s \in sat(\Phi_1)$ and $\exists s' \in T.s \rightarrow s'$ implies $s \in T$.

7.
$$sat(\exists \Box \Phi)$$
 is the *largest* subset T of S such that
7.1 $T \subseteq sat(\Phi)$ and
7.2 $s \in T$ implies $\exists s' \in T.s' \to s$.



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Example $\exists (\top U (a = c) \land (a \neq b))$



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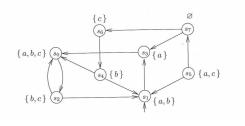
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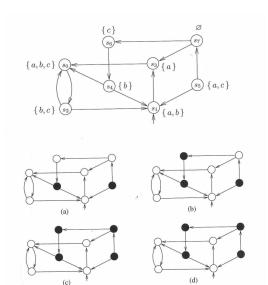
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Example $\exists (\top U (a = c) \land (a \neq b))$





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Symbolic?



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"Symbolic Model Checking: $10^{20}\ {\rm States}$ and beyond" [1]

- explicit state vs. symbolic
- normal forms

Switching functions

"bit-vectors"

$$Eval(Var) = Var \rightarrow \{0, 1\}$$

Switching function

$$f: Eval(Var) \to \{0, 1\} = (Var \to \{0, 1\}) \to \{0, 1\}$$

cf. propositional semantics $[\![\varphi]\!]$



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Boolean operators on switching functions



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• cf. truth tables

- all boolean operators and constants have (of course) an analogue on switching functions
- syntax vs. semantics
- canonical (and/or normal) forms

Operators on switching functions (2)

Projection of a "bit-vector" onto a variable

$$proj_{z_i}: (Var \to \{0,1\}) \to \{0,1\}$$

Disjunction



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Operators on switching functions (2)

Projection of a "bit-vector" onto a variable

$$proj_{z_i} : (Var \to \{0, 1\}) \to \{0, 1\}$$

Disjunction

$$\begin{array}{l} (f_1 \vee f_2)([z_1 = b_1, \ldots, z_k = b_k]) = \\ \max(f_1([z_1 = b_1, \ldots, z_m = b_m]), f_2([z_n = b_n, \ldots, z_k = b_k]) \stackrel{\text{Checking}}{\Longrightarrow} \\ \end{array}$$



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Cofactors

Definition (Cofactors)

Assume a variable set $Var = \{z, y_1, \ldots, y_m\}$ and let $f : (Var \rightarrow \{0, 1\}) \rightarrow \{0, 1\}$ be a switching function over it. The *positive cofactor* of f for variable z, written $f \mid_{z=1}$ is the switching function given by

$$f|_{z=1} (b_1, \dots, b_m) = f(1, b_1, \dots, b_m)$$
 (2)

The negative cofactor of f for z, written $f |_{z=0}$ is defined analogously. If f is a switching function for $\{z_1, \ldots, z_k, y_1, \ldots, y_m\}$, then we write $f |_{z_1=b_1,\ldots,z_k=b_k}$ for the *iterated cofactor* of f, given by

$$f \mid_{z_1=b_1,\dots,z_k=b_k} = (\dots (f \mid_{z=b_1}) \dots) \mid_{z_k=b_k}$$
(3)



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Essential variables

Definition (Essential variable)

A variable z is essential for a switching function, if

$$f \mid_{z=1} \neq f \mid_{z=0} .$$



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Shannon expansion

Lemma (Shannon expansion)

Let f be a switching function for Var. Then

$$f = (\neg z \land f \mid_{z=1}) \lor (z \land f \mid_{z=0})$$

for all variables z from Var.



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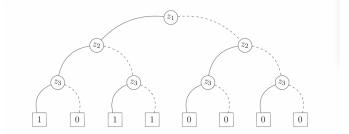
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Binary decision tree $z_1 \wedge (\neg z_2 \vee z_3)$





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Propositional encoding

$$\mathcal{T} = (S, \to, I, P, L)$$

Task: encode by boolean formulas / switching functions

- all ingredients of ${\mathcal T}$
- sat(_)
- realise operations during the mc-algo by operations on the encodings, in particular pre



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Characteristic function

Equivalent (isomorphic) views

•
$$2^S \equiv S \rightarrow \{0,1\} \text{ (or } S \rightarrow \mathbb{B})$$

•
$$B \subseteq S$$
 χ_B



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Encoding states and subsets of states

- that this is possible, should be obvious
- use propositional variables x_1, \ldots, x_n
- padding: assume $S = Eval(\vec{x}) = \{x_1, \dots, x_n\} \rightarrow \{0, 1\}$

Sets of states $B \subseteq S$

$$\chi_B : (Eval(\vec{x}) \to \{0, 1\} = (\vec{x} \to \{0, 1\}) \to \{0, 1\}$$



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Encoding the transition relation ightarrow

$$\rightarrow \subseteq S \times S$$

- make a "copy" of \vec{x} : different variables \vec{x}'
- renaming operation on switching functions [y ← x]

 $\mathbf{Encode} \to \mathbf{as}\ \Delta$

$$\begin{split} \Delta &: ((\vec{x}, \vec{x}') \to \{0, 1\}) \to \{0, 1\}, \\ \Delta(s_1, s_2[\vec{x}' \leftarrow \vec{x}]) &= \begin{cases} 1 & \text{if } s_1 \to s_2 \\ 0 & \text{else} \end{cases} \end{split}$$



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Remember charaterization and the fixpoints

1.
$$sat(\top) = S$$
.

2.
$$sat(p) = \{s \in S \mid p \in V(s), \text{ for any } p \in P\}.$$

3.
$$sat(\Phi_1 \land \Phi_2) = sat(\Phi_1) \cap sat(\Phi_2).$$

4.
$$sat(\neg \Phi) = S \setminus sat(\Phi)$$
.

5.
$$sat(\exists \bigcirc \Phi) = \{s \in S \mid \exists s'.s \to s' \land s' \in sat(\Phi)\}$$

6.
$$sat(\exists (\Phi_1 \ U \ \Phi_2))$$
 is the *smallest* subset T of S such that

6.1
$$sat(\Phi_2) \subseteq T$$
 and

6.2
$$s \in sat(\Phi_1)$$
 and $\exists s' \in T.s \to s'$ implies $s \in T$.

7.
$$sat(\exists \Box \Phi)$$
 is the *largest* subset T of S such that
7.1 $T \subseteq sat(\Phi)$ and
7.2 $s \in T$ implies $\exists s' \in T.s' \to s$.



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$\exists \bigcirc A$ means, calculating *pre*

pre-calulation as switching function

$$\exists \vec{x}'. \underbrace{\Delta(\vec{x}, \vec{x}')}_{s \in pre(s')} \land \underbrace{\chi_A[\vec{x}' \leftarrow \vec{x}]}_{s' \in A}$$



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$\exists \Diamond$: basically iteration over *pre*

One step:
$$T_{j+1} = pre(T_j)$$

$$\exists \vec{x}' . \underbrace{\Delta(\vec{x}, \vec{x}')}_{s \in pre(s')} \land \underbrace{f_j[\vec{x}' \leftarrow \vec{x}]}_{s' \in T_j}$$



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\exists U: not much harder...



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 $\begin{array}{l} f_0(\vec{x}) := \chi_{A_1}(\vec{x}); \\ j := 0; \\ \text{repeat} \\ f_{j+1}(\vec{x}) := f_{j+1}(\vec{x}) \lor (\chi_{A_2}(\vec{x}) \land \exists \vec{x}'. \Delta(\vec{x}, \vec{x}') \land f_j(\vec{x}')); \\ \text{until } f_j(\vec{x}) = f_{j-1}(\vec{x}); \\ \text{feturn } f_j(\vec{x}). \end{array}$

finally $\exists \Box$

 $\begin{array}{l} 1 \\ f_0(\vec{x}) &:= \chi_A(\vec{x}); \\ j &:= 0; \\ \\ \mathbf{repeat} \\ f_{j+1}(\vec{x}) &:= f_{j+1}(\vec{x}) \land \exists \vec{x}'. \Delta(\vec{x}, \vec{x}') \land f_j(\vec{x}')); \\ \mathbf{until} & f_j(\vec{x}) = f_{j-1}(\vec{x}); \\ \mathbf{return} & f_j(\vec{x}). \end{array}$

Largest fixpoint

sets get smaller in the iteration



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Section

Binary decision diagrams

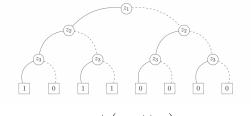
Chapter 4 "CTL and CTL*" Course "Model checking" Martin Steffen Autumn 2021

How to efficiently implement all that?

switching functions

$$f: Eval(Var) \rightarrow \{0,1\} = (Var \rightarrow \{0,1\}) \rightarrow \{0,1\}$$

- + operations thereon
- binary decision trees



 $z_1 \wedge (\neg z_2 \vee z_3)$



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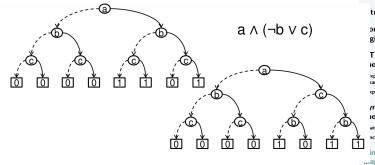
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Issues

- size: still exponential
- non-canonical



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inary decision

From BDTs to (RO)BDDs

- addressing both problems
- reduced ordered binary decision diagrams
- often "BDDs" just mean ROBDDs
- two general ideas

Two general ideas

to addess both mentioned problems.

Canonicity: fix an order on the variables

Size: don't represent duplicate parts of the graph more than once



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As first easy step

BDT to **OBDD**

- have only 2 terminal nodes (for 0 and for 1), no duplicate leaves (BDD), and
- fix an order on the var's (OBDD)



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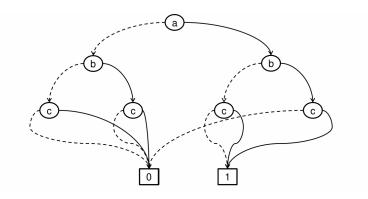
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Example OBDD



 $a \wedge (\neg b \lor c)$ with order a < b < c



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Reduced OBBDs

Reduce

Uniqueness

no 2 nodes for the same variable have the "same" high- and low-children \Rightarrow merge isomorphic subgraphs

non-redundent tests

no variable node has identical high and low children



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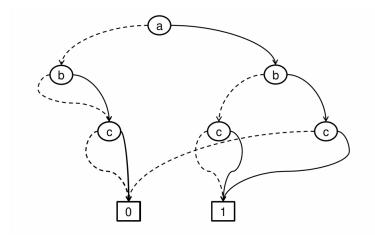
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Merge isomorphic subgraphs



 $a \wedge (\neg b \lor c)$ with order a < b < c



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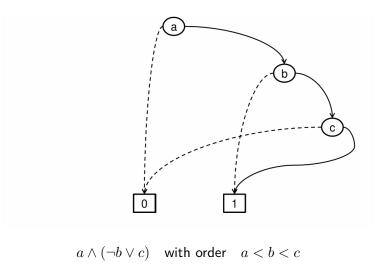
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Remove reduncancy





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ROBDDs as canonical representation

Canonicity

For every boolean function $f: (Var \rightarrow \{0,1\}) \rightarrow \{0,1\}$ and a give variable ordering, there exists exactly one ROBDD representing f

facts

equivalence checking

satisfiability



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ROBDDs as canonical representation

Canonicity

For every boolean function $f : (Var \rightarrow \{0,1\}) \rightarrow \{0,1\}$ and a give variable ordering, there exists exactly one ROBDD representing f

facts

equivalence checking

linear time

satisfiability

constant time



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But where is the catch?

Satisfiability

Isn't SAT NP-complete?



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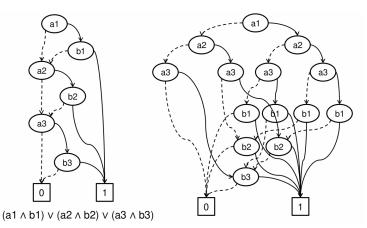
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Sensitivity to variable order





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Sensitivity to variable order

- different variable orders \Rightarrow different ROBBDs
- crucial in practice to find a (in many cases) good order
- finding the best: NP-hard
- heuritics exists



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Representing boolean functions is not all

- canonical, often (but not always) compact representation
- we also need to "work" with them
- remember the CTL model checking algorithms



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Boolean operators (Apply)

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Binary decision diagrams

- boolean operators on (R)OBBDs
- recursively over the two OBDDs
- based on Shannon's (or Boole's) expansion
- preserve the order
- if working on ROBBs, re-reduce the result.

Logical operations on OBDDs

Logical negation (\neg)

 Replace the value of each leaf node by its negation
 All 16 logical operations can be applied on boolean functions using the Apply algorithm.

Restriction of the variable x_i to a constant b:

- $f|_{x_i \leftarrow b}(x_1, \ldots, x_n) = f(x_1, x_2, \ldots, x_{i-1}, b, x_{i+1}, \ldots, x_n)$
- ▶ $f|_{x_i \leftarrow 1}$: positive Shannon cofactor of f for x_i
- ▶ $f|_{x_i \leftarrow 0}$: negative Shannon cofactor of f for x_i
- ▶ To compute the new OBDD:
 - We traverse the tree in a depth-first manner
 - All incoming edges to v, s.t. var(v) = x_i should be redirected to low(v) if b = 0 or high(v) if b = 1
 - Reduce the OBDD



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Apply

Shannon expansion:

- $f = (\neg x \land f|_{x \leftarrow 0}) \lor (x \land f|_{x \leftarrow 1})$
- Allows us to split a problem into two subproblems

Using the Apply algorithm to solve all 16 logical operations. Let

- • be a two-argument logical operation (and, or, xor etc.)
- ▶ f and f' be two boolean functions
- v and v' be the OBDDs roots for f and f'

•
$$var(v) = x$$
 and $var(v') = x'$

If both v and v' are drains:

•
$$f \bullet f' = val(v) \bullet val(v')$$



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Apply (cont'd)



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Working with BDDs

If x = x':

Recursively solve the two subproblems:

 $f \bullet f' = (\neg x \land (f|_{x \leftarrow 0} \bullet f'|_{x \leftarrow 0})) \lor (x \land (f|_{x \leftarrow 1} \bullet f'|_{x \leftarrow 1}))$

- The root of this new OBDD will be a new node w such that
 - var(w) = x
 - ► low(w) will be OBDD for $(f|_{x \leftarrow 0} \bullet f'|_{x \leftarrow 0})$
 - high(w) will be OBDD for $(f|_{x\leftarrow 1} \bullet f'|_{x\leftarrow 1})$

If x < x' ($x = x_i$ and $x' = x_j$ where i < j):

- ► $f \bullet f' = (\neg x \land (f|_{x \leftarrow 0} \bullet f')) \lor (x \land (f|_{x \leftarrow 1} \bullet f'))$
- ▶ Similar for x > x'

Algorithm is polynomial with dynamic programming

Boolean quantification

If f is a function, x is a variable, then

$$\blacktriangleright \exists x.f = (f|_{x \leftarrow 0}) \lor (f|_{x \leftarrow 1})$$

$$\blacktriangleright \forall x.f = (f|_{x \leftarrow 0}) \land (f|_{x \leftarrow 1})$$

We need to compute the OBDD for both subproblems using the *Restrict* algorithm:

- $f|_{x \leftarrow 0}$: For each node v where var(v) = x
 - Incoming edges are redirected to low(v)
 - Remove node v

•
$$f_{x \leftarrow 1}$$
: For each node v where $var(v) = x$

- Incoming edges are redirected to high(v)
- Remove node v



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Bibliography

- Burch, J. R., Clarke, E. M., McMillan, K. L., Dill, D. L., and Hwang, L. (1992). Symbolic model checking: 10²⁰ states and beyond. *Information and Computation*, 98(2):142–170.
- [2] Clarke, E. M. and Emerson, E. A. (1982). Design and synthesis of synchronisation skeletons using branching time temporal logic specifications. In Kozen, D., editor, *Proceedings of the Workshop on Logic of Programs 1981*, volume 131 of *Lecture Notes in Computer Science*, pages 244–263. Springer Verlag.
- [3] Queille, J. P. and Sifakis, J. (1982). Specification and verification of concurrent systems in CESAR. In Dezani-Ciancaglini, M. and Montanari, U., editors, Proceedings of the 5th International Symposium on Programming 1981, volume 137 of Lecture Notes in Computer Science, pages 337–351. Springer Verlag.