

Chapter 5

Sat-based & Bounded model checking

Course "Model checking" Martin Steffen Autumn 2021



Section

Introduction

Chapter 5 "Sat-based & Bounded model checking" Course "Model checking" Martin Steffen Autumn 2021

Model checking

 $S\models^?\varphi$

- origin [7]¹ & [11]
- $S \pmod{\text{of the}}$ system,
- φ : formula in a suitable logic
 - LTL
 - CTL, CTL*, modal μ -calculus

• . . .

 ultimately a fancy "graph exploration problem" (with big graphs)



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

¹the conference was 1981, the book was published 1982

Advantages of MC



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

- no proofs, "push button"
- diagnostic counterexamples
- logics used for MC can express many concurrency problems

Main "disadvantage"

- state space explosion problem (aka state explosion problem)
- problem "solution" space grows *exponential* is the problem "description" space
 - notably reachable state space exponential in the number of processes



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

The 4 big breakthroughs combatting the SSEP

Apart from

- advances in data structures,
- software engineering,
- tricks, optimizations, heuristics and
- general advances in processing power/memory.

Clarke identifies the following

"big 4" breakthroughs

- 1. symbolic techniques (notably using BDDs)²
- 2. partial order reduction
- 3. bounded model checking
- 4. CEGAR, localisation reduction [9] [4] [3]



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT



Section

SAT solving and SMT

Chapter 5 "Sat-based & Bounded model checking" Course "Model checking" Martin Steffen Autumn 2021



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

- (boolean) satisfiability
- famous, prototypical NP-complete problem

SAT solver progress

- highly competitive field
- yearly "SAT-competition"³





IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Reducing bounded model checking to SAT

taken from [6]

³http://www.satcompetition.org/

Bounded model checking



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Reducing bounded model checking to SAT

• Origin: [1] (see also [2])

BMC starting point

Leverage sat-solving, a powerful a successful technique, to do model checking

Cf.: Symbolic model checking and BDDs

- See separate presentation
- successful technique
- used (most prominently for HW) in industrial uses of MC
- Two ingredients of SMC
 - operating symbolically on representation of sets of states
 - use *BDDs* (= specific kind of graph representation of *boolean* functions) to represent and operate on them
- like SMC/BDD-based MC: BMC based on "boolean encodings"



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Bad news: the MC problem/reachability is *not* a SAT problem :-(

- MC here:⁴
 - models are kind of transition systems/Kripke structures
 - spec's are "temporal logic" formulas

solving an MC problem

It all boils down to some form of fancy graph reachability

- "reachability", however:
 - a form of "fixpoint" calculation⁵
 - fixpoints are emphatically not part of boolean logic.⁶

⁴The term "model checking", i.e., solving $M \models^? \varphi$ can be applied in different settings as well. A boolean assignment can be seen as *model* of a propositional formula, for instance. That *is* of course a SAT problem. But we are interested transition systems satisfying a TL formula.

⁵see also the presentation about μ -calculus.

⁶They are not even part of first-order logic. Implicitly they are part in temporal logics, though (eventually, until etc.)



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Good news: *bounded* MC can be seen as SAT :-)

less ambitious goal

Can I find an error (conterexample) in the behavior of the system considering up-to k steps from the initial states

- price to pay: no more "verification"⁷
- bug-hunting
- simple core idea

IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

⁷but MC is typically verification of a model/abstraction anyhow and/or verification up until the MC runs out of time/memory.

LTL and "existential" LTL

• remember: LTL (linear time temporal logic) and definition of



- φ must hold for all paths of S
- If $S \not\models \varphi$ (error), then exists a paths π such that $\pi \not\models \varphi$

For explicitness' sake

path quantifiers⁸

 $\forall \varphi \text{ and } \exists \varphi$

assume NNF



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

⁸one single quantifier as prefix to an LTL formula.

Terminology: witnesses

counterexample for

$$S \models \Box p$$
 corresponds to $S \models \forall \Box p$

corresponds to the question if there exists a witness⁹

 $\supset \neg p$

- Goal: find finite (fixed bound) prefixes as witness to an existential model checking problem (LTL)
- conceptually easy if original $\forall \varphi$ is a safety prop.
- liveness? witness for ∃□?



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

```
SAT solving and SMT
```

⁹in logics in general, a witness is a thing (here a path) that gives (constructive) evidence to an existential formula

Terminology: witnesses

counterexample for

$$S \models \Box p$$
 corresponds to $S \models \forall \Box p$

corresponds to the question if there exists a witness⁹

 $\Diamond \neg p$

- Goal: find finite (fixed bound) prefixes as witness to an existential model checking problem (LTL)
- conceptually easy if original $\forall \varphi$ is a safety prop.
- liveness? witness for $\exists \Box$? \Rightarrow loops



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

```
SAT solving and SMT
```

⁹in logics in general, a witness is a thing (here a path) that gives (constructive) evidence to an existential formula

Paths with and without loops



• only prefix with back loop can be witness for $\Box p$





IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Loops

Given: TS/Kripke-structure. transition relation \rightarrow .

Definition

Assume $l \leq k$. A path π is a (k, l)-loop if $\pi_k \rightarrow \pi_l$ and

$$\pi = u \cdot v^{\omega}$$

with

$$u = \pi_0 \dots \pi_{l-1}$$
 and $v = \pi_l \dots \pi_k$

A path π is a k-loop if there exists an l with $0\leq l\leq k$ s.t. π is a (k,l)-loop

- remember: paths π are (infinite) sequences of "states" (worlds)
- loops here is about those states (not "edges" of the picture)



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

(1)

SAT solving and SMT

Bounded semantics

- remember the "normal" semantics of LTL from before, relating formulas and paths
- $\llbracket \varphi \rrbracket$ or $\pi \models \varphi$
- now: the new "looping paths" (k-loops) as basis for bounded semantics, i.e., basis for BMC
- note: "finite" prefixes (loops) can give information for infinite paths, thus serve as witnesses
- boundes semantics for path

 with loop: "unchanged"
 without loop: be aware of the cut-off and be
 pessimistic



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Bounded semantics: for loops

Definition (Bounded semantics: with lasso)

Let π be a k-loop. A formula φ is valid along π with bound k, written

$$\pi \models_k \varphi$$
,

 $\text{iff }\pi\models\varphi.$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Bounded semantics: without loops

Definition

Let π be a path which is *not* a k-loop. Then an LTL formula φ is *valid along* π *with bound* k, written

$$\pi \models_k \varphi$$

iff $\pi \models^0_k \varphi$, given below.

- earlier $\pi \models \varphi$, corresponding here to \models^0
- k is treated as "cut-off":
- what comes afterward: unknown
- if in doubt: "false", i.e., the path is not valid/does not satisfy the formula in the bounded manner
 - for \bigcirc : don't "look" beyond k
 - for \Box : be pessimistic
 - for $\Diamond:$ positive answer at least possible within the bound



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Bounded semantics: without loops (\models_k^i)

Definition (Bounded semantics: without lasso) IN5110 -Verification and specification of parallel systems Martin Steffen $\pi \models^i_k p$ iff $p \in L(\pi_i)$ $\pi \models_{L}^{i} \neg p$ iff $p \notin L(\pi_i)$ Introduction $\pi \models^i_k \varphi_1 \land \varphi_2$ iff $\pi \models_{k}^{i} \varphi_{1}$ and $\pi \models_{k}^{i} \varphi_{2}$ SAT solving and SMT $\pi \models^i_k \varphi_1 \lor \varphi_2$ iff $\pi \models_k^i \varphi_1$ or $\pi \models_k^i \varphi_2$ Reducing bounded model checking to SAT $\pi \models^{i}_{h} \Box \varphi$ is always false iff $\exists j.i \leq j \leq k$. $\pi \models^{j}_{k} \varphi$ $\pi \models^i_k \Diamond \varphi$ iff i < k and $\pi \models_k^{i+1} \varphi$ $\pi \models^i_k \bigcirc \varphi$ $\pi \models^i_k \varphi_1 U \varphi_2$ iff $\exists j, i \leq j \leq k.\pi \models_k^j \varphi_2$ and $\forall n, i \leq n < j.\pi \models_k^n \varphi_1$ $\pi \models^i_k \varphi_1 R \varphi_2$ iff $\exists j, i \leq j \leq k.\pi \models_k^j \varphi_1$ and $\forall n, i \leq n < j.\pi \models_k^n \varphi_2$

$\textbf{Bounded} \rightarrow \textbf{unbounded semantics}$

- Note, the connection is done for existential LTL (formulas of the form ∃φ, not like ∀φ)
- unbounded semantics as limit of the bounded ones (for all/arbitrary bounds k)

Lemma (Easy direction (per path))

$$\pi \models_k \varphi$$
 implies $\pi \models \varphi$

Lemma (For TSs/KSs)

$$S \models \exists \varphi \quad implies \quad S \models_k \exists \varphi \quad for some \ k \ge 0$$

Theorem

$$S \models \exists \varphi \quad \text{iff} \quad S \models_k \exists \varphi \quad \text{ for some } k \ge 0$$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT



Section

Reducing bounded model checking to SAT

Chapter 5 "Sat-based & Bounded model checking" Course "Model checking" Martin Steffen Autumn 2021

BMC via SAT

- so far:
 - definition of the bounded MC problem
 - we convinced ourself: BMC approximates MC (at least for existential path formulas)
- Now: reduce to sat-solving

Goal

 $[\![S,\varphi]\!]_k$ is satisfiable iff π is a witness for φ

- sat -problems: formula with (propositional) variables
- encoding given in 3 parts. given k
 - 1. valid initial path for S and
 - 2. satisfaction of formula if
 - there's a loop or
 - there's no loop
- remember symbolic CTL model checking



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Kripke-structure/transition system

Definition (Kripke structure)

A Kripke structure or transition system is a tuple (S, I, \rightarrow, V) where S is the set of states, $I \subseteq S$ the set of initial states, $\rightarrow \subseteq S \times S$ the transition relation, and $V: S \rightarrow 2^P$ the valuation function (aka. (state) labelling function).

- transition relation: a predicate: $^{10} \rightarrow : S^2 \rightarrow \mathbb{B}$
- initial states: a predicate $I : S \to \mathbb{B}$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

 $^{^{10}}$ [2] write $T(s_1,s_2)$ for our infix relational notation $s_1 \rightarrow s_2$, where T is the transition relation predicate.

$\mathbf{1}^{st}$ component: Translating S



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

- remember transition system/Kripke stuctures ${\cal S}$
 - states s_i . Consider s_i as variables
 - transition relation: as predicate $T(s_k,s_l),$ we write still infix $s_k \rightarrow s_l$
- unfolding of the transition relation

$$\llbracket T \rrbracket_k \triangleq I(s_0) \land \bigwedge_{i=0}^{k-1} s_i \to s_{i+1}$$
⁽²⁾

- remember in CTL how we encoded $S \times S$
- states in KS: propositional variables sk

Loop condition

• Remember the def. of (k, l)-loop



simple abbreviation

$${}_{l}L_{k} \triangleq s_{k} \to s_{l}$$

• loop condition holds¹¹ iff there is a back loop from a state s_k back to a previous state s_l (which can be s_k)

Definition (Loop condition)

$$L_k \triangleq \bigvee_{l=0}^k {}_l L_k$$

¹¹resp. it will hold when applied to a path consisting of a sequence of states s_i , which are considered as propositional variables, as said. the word "back" makes sense only if one interprets the variables to be "in a sequence".



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Successor in a loop

a rather unsurprising definition: define "successor"

succ(i) of i in a (k, l)-loop as

•
$$succ(i) = i + 1$$
 for $i < k$

•
$$succ(i) = l$$
 for k



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

2^{*nd*} component: translating formula with a loop

propositional part: boring

$$\begin{split} {}_{l}\llbracket p \rrbracket_{k}^{i} &\triangleq p(s_{i}) \\ {}_{l}\llbracket \neg p \rrbracket_{k}^{i} &\triangleq \neg p(s_{i}) \\ {}_{l}\llbracket \varphi_{1} \wedge \varphi_{2} \rrbracket_{k}^{i} &\triangleq {}_{l}\llbracket \varphi_{1} \rrbracket_{k}^{i} \wedge {}_{l}\llbracket \varphi_{2} \rrbracket_{k}^{i} \\ {}_{l}\llbracket \varphi_{1} \vee \varphi_{2} \rrbracket_{k}^{i} &\triangleq {}_{l}\llbracket \varphi_{1} \rrbracket_{k}^{i} \vee {}_{l}\llbracket \varphi_{2} \rrbracket_{k}^{i} \end{split}$$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Cont'd

Actually straightforward

- loop \rightarrow no cut-off \rightarrow "standard semantics"
- remember *unrolling* of fixpoints¹²

temporal part: a bit more interesting

$$\begin{split} \iota \llbracket \Box \varphi \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi \rrbracket_{k}^{i} \wedge \iota \llbracket \Box \varphi \rrbracket_{k}^{succ(i)} \\ \iota \llbracket \Diamond \varphi \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi \rrbracket_{k}^{i} \vee \iota \llbracket \Diamond \varphi \rrbracket_{k}^{succ(i)} \\ \iota \llbracket \bigcirc \varphi \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi \rrbracket_{k}^{succ(i)} \\ \iota \llbracket \bigcirc \varphi \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi \rrbracket_{k}^{succ(i)} \\ \iota \llbracket \varphi_{1} \ U \ \varphi_{2} \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi_{1} \rrbracket_{k}^{i} \vee \iota \llbracket \varphi_{1} \ U \ \varphi_{2} \rrbracket_{k}^{succ(i)} \\ \iota \llbracket \varphi_{1} \ R \ \varphi_{2} \rrbracket_{k}^{i} &\triangleq \iota \llbracket \varphi_{2} \rrbracket_{k}^{i} \wedge \iota \llbracket \varphi_{1} \ R \ \varphi_{2} \rrbracket_{k}^{succ(i)} \end{split}$$

 $^{12}{\rm Cf.}$ also the presentation about the $\mu\text{-}{\rm calculus.}$ Also in the construction of the Büchi-automaton from an LTL formula, that



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Translation without a loop

- same principles
- "index" *l* not needed
- instead of the more complex succ(i): simply i + 1.
- otherwise: the definition stays "the same")



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

3^{*rd*} **component: translating formula without a loop**

Inductive case $\forall i \leq k$:

propositional part: boring again

$$\begin{bmatrix} p \end{bmatrix}_{k}^{i} \triangleq p(s_{i}) \\ \begin{bmatrix} \neg p \end{bmatrix}_{k}^{i} \triangleq \neg p(s_{i}) \\ \begin{bmatrix} \varphi_{1} \land \varphi_{2} \end{bmatrix}_{k}^{i} \triangleq \begin{bmatrix} \varphi_{1} \end{bmatrix}_{k}^{i} \land \begin{bmatrix} \varphi_{2} \end{bmatrix}_{k}^{i} \\ \begin{bmatrix} \varphi_{1} \lor \varphi_{2} \end{bmatrix}_{k}^{i} \triangleq \begin{bmatrix} \varphi_{1} \end{bmatrix}_{k}^{i} \lor \begin{bmatrix} \varphi_{2} \end{bmatrix}_{k}^{i}$$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Loop-case (cont'd)

Inductive case $\forall i \leq k$:

temporal part: a bit more interesting

$$\begin{split} \llbracket \Box \varphi \rrbracket_{k}^{i} &\triangleq \llbracket \varphi \rrbracket_{k}^{i} \wedge \llbracket \Box \varphi \rrbracket_{k}^{i+1} \\ \llbracket \Diamond \varphi \rrbracket_{k}^{i} &\triangleq \llbracket \varphi \rrbracket_{k}^{i} \vee \llbracket \Diamond \varphi \rrbracket_{k}^{i+1} \\ \llbracket \bigcirc \varphi \rrbracket_{k}^{i} &\triangleq \llbracket \varphi \rrbracket_{k}^{i+1} \\ \llbracket \bigcirc \varphi \rrbracket_{k}^{i} &\triangleq \llbracket \varphi \rrbracket_{k}^{i+1} \\ \llbracket \varphi_{1} \ U \ \varphi_{2} \rrbracket_{k}^{i} &\triangleq \llbracket \varphi_{1} \rrbracket_{k}^{i} \vee \llbracket \varphi_{1} \ U \ \varphi_{2} \rrbracket_{k}^{i+1} \\ \llbracket \varphi_{1} \ R \ \varphi_{2} \rrbracket_{k}^{i} &\triangleq \llbracket \varphi_{2} \rrbracket_{k}^{i} \wedge \llbracket \varphi_{1} \ R \ \varphi_{2} \rrbracket_{k}^{i+1} \end{split}$$



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

Reducing bounded model checking to SAT

• base case: $\llbracket \varphi \rrbracket_k^{k+1} \triangleq false$

Putting it together



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

(3)

SAT solving and SMT

Reducing bounded model checking to SAT

$$\begin{split} \llbracket S, \varphi \rrbracket_k &\triangleq & \llbracket S \rrbracket_k \land \\ & (& (\neg L_k \land \llbracket \varphi \rrbracket_k^0) \\ & \lor & (\lor_{l=0}^k (_l L_k \land \ _l \llbracket \varphi \rrbracket_k^0)) \end{split}$$

Theorem

 $\llbracket S, \varphi \rrbracket_k$ satisfiable iff $S \models_k \exists \varphi$.



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

- The technical slides here recap parts of the journal article [2] by the inventors of BMC
- BMC for software [8]
- Survey [10]

References I

Bibliography

- Biere, A., Cimatti, A., Clarke, E. M., Fujita, M., and Zhu, Y. (2009). Symbolic model checking using SAT procedures instead of BDDs. In *Proceedings of DAC'09: Design Automation Conference*, pages 317–320. ACM.
- [2] Biere, A., Cimatti, A., Clarke, E. M., Strichman, O., and Zhu, Y. (2003). Bounded model checking. Advances in Computers, 58(11):117–148.
- [3] Clarke, E., Grumberg, O., Jha, S., Lu, Y., and Veith, H. (2000). Counterexample-guided abstraction refinement. In Emerson, E. A. and Sistla, A. P., editors, *Proceedings of the 12th International Conference on Computer-Aided Verification (CAV '00)*, volume 1855 of *Lecture Notes in Computer Science*, pages 154–169. Springer Verlag.
- [4] Clarke, E. C., Kurshan, R. P., and Veith, H. (2010). The localization reduction and counter-examble guided abstraction refinement. In Manna, Z. and Peled, D., editors, *Pnueli Festschrift*, volume 6200 of *Lecture Notes in Computer Science*, pages 61–71. Springer Verlag.
- [5] Clarke, E. M. (2008). Model checking my 27-year quest to overcome the state explosion problem. In Cervesato, I., Veith, H., and Voronkov, A., editors, Logic for Programming, Artificial Intelligence, and Reasoning: 15th International Conference, LPAR 2008, Doha, Qatar, November 22-27, 2008. Proceedings, Lecture Notes in Artificial Intelligence, pages 182–182. Springer Verlag.
- [6] Clarke, E. M. (2017). SAT-based bounded and unbounded model checking. Available electronically on the net. Data of publication unknown.
- [7] Clarke, E. M. and Emerson, E. A. (1982). Design and synthesis of synchronisation skeletons using branching time temporal logic specifications. In Kozen, D., editor, *Proceedings of the Workshop on Logic of Programs 1981*, volume 131 of *Lecture Notes in Computer Science*, pages 244–263. Springer Verlag.
- [8] Kroening, D., Lerda, F., and Clarke, E. (2004). Bounded model checking for software. In Jensen, K. and Podelski, A., editors, *Proceedings of TACAS 2004*, volume 2988 of *Lecture Notes in Computer Science*. Springer Verlag.
- [9] Kurshan, R. P. (1993). Automata Theoretic Verification of Coordinating Processes. Princeton University Press.



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

References II



IN5110 – Verification and specification of parallel systems

Martin Steffen

Introduction

SAT solving and SMT

- [10] Prasad, M. R., Biere, A., and Gupta, A. (2005). A survey of recent advances in sat-based formal verification. International Journal on Software Tools for Technology Transfer, 7(2):156–173.
- [11] Queille, J. P. and Sifakis, J. (1982). Specification and verification of concurrent systems in CESAR. In Dezani-Ciancaglini, M. and Montanari, U., editors, *Proceedings of the 5th International Symposium on Programming 1981*, volume 137 of *Lecture Notes in Computer Science*, pages 337–351. Springer Verlag.