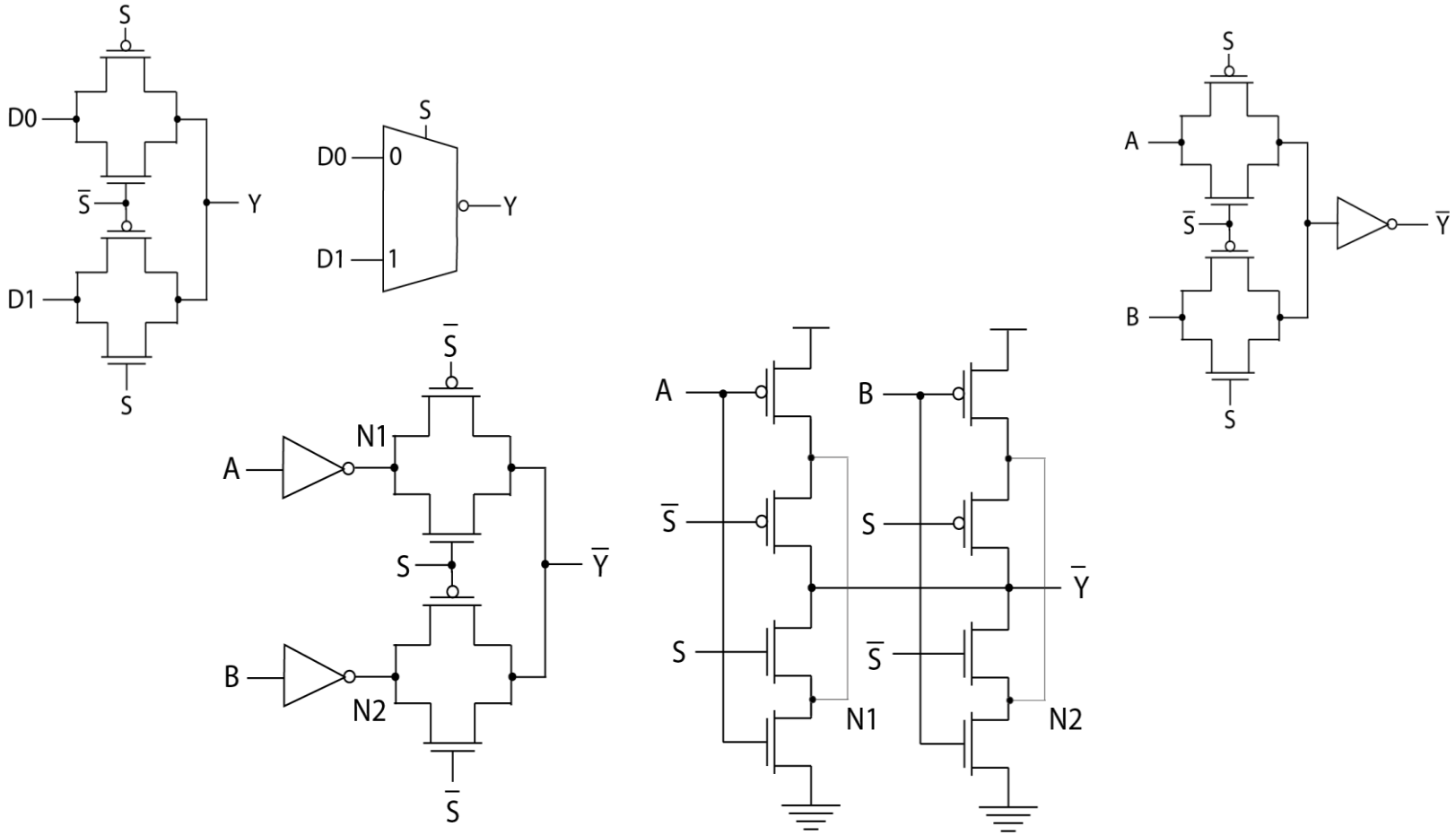
UiO : Universitetet i Oslo

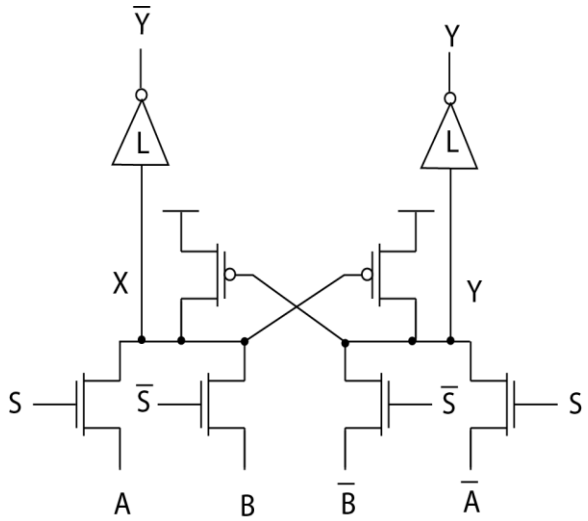
INF3400 Del 12 Teori

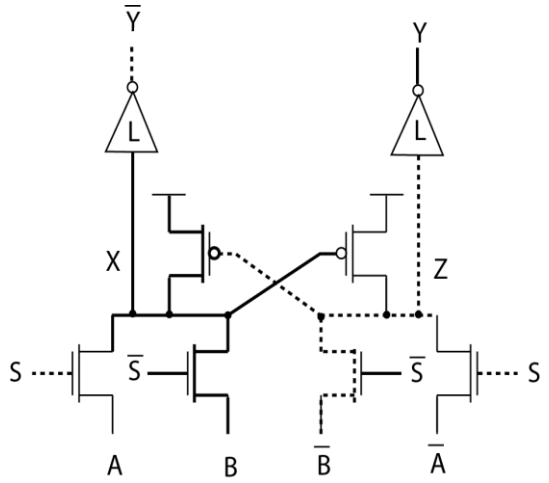
Passtransistor- og differensiell CMOS logikk

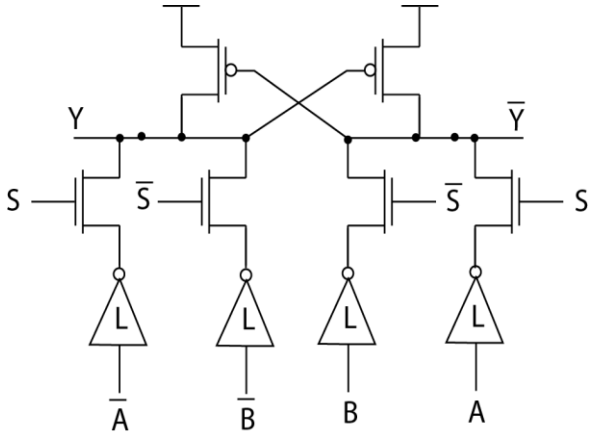
CMOS med transmisjonsporter

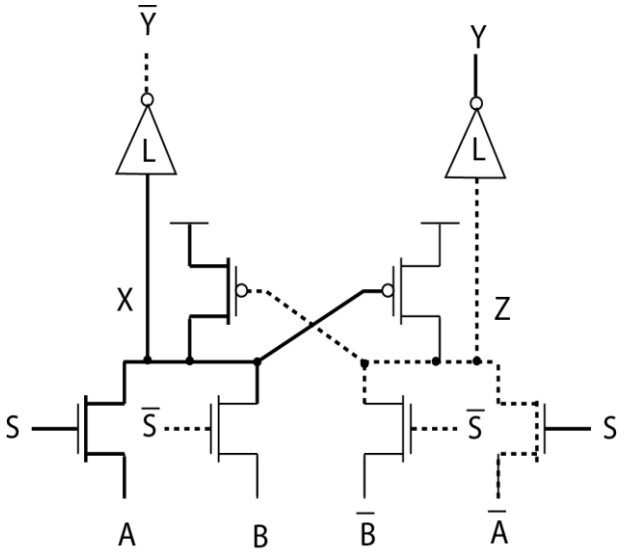


Komplementær pass transistor logikk (CPL)

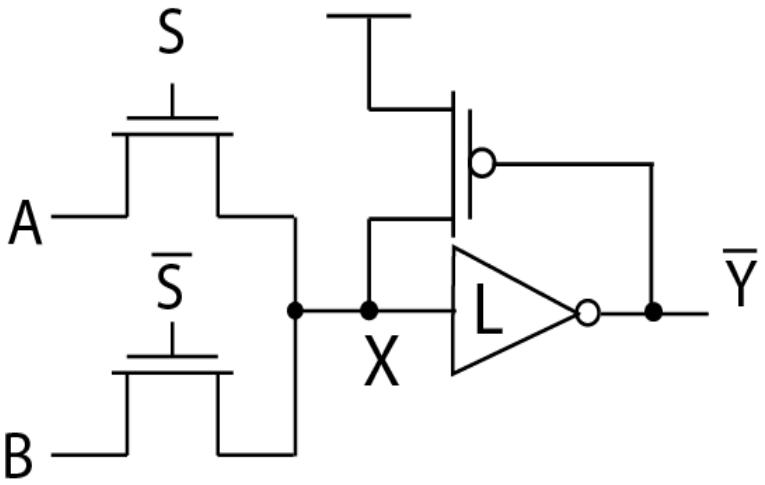




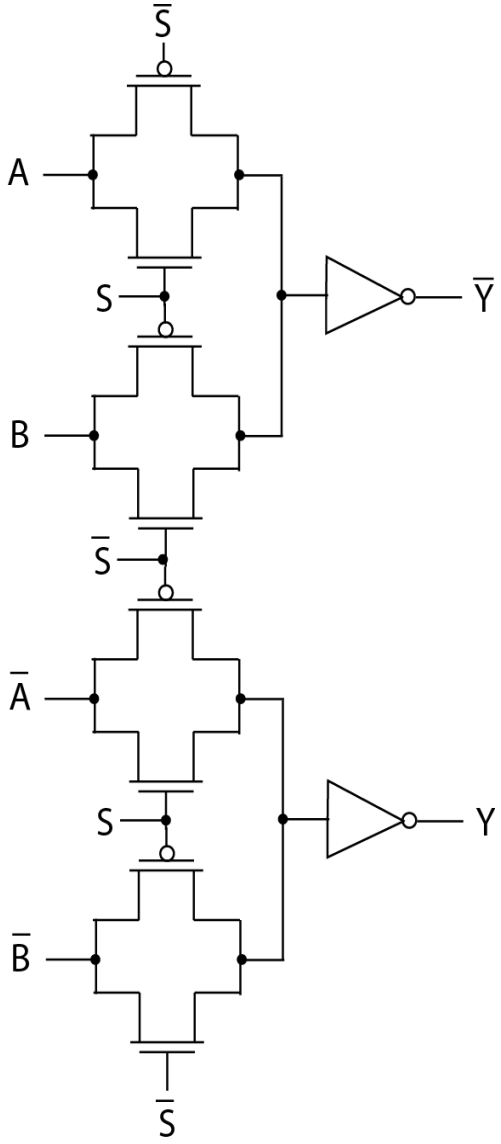


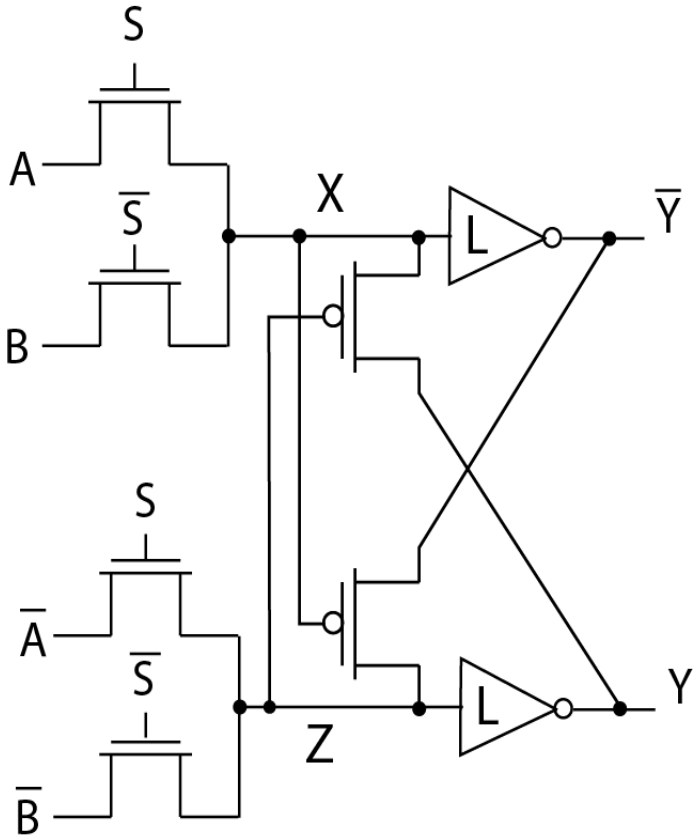


LEAP

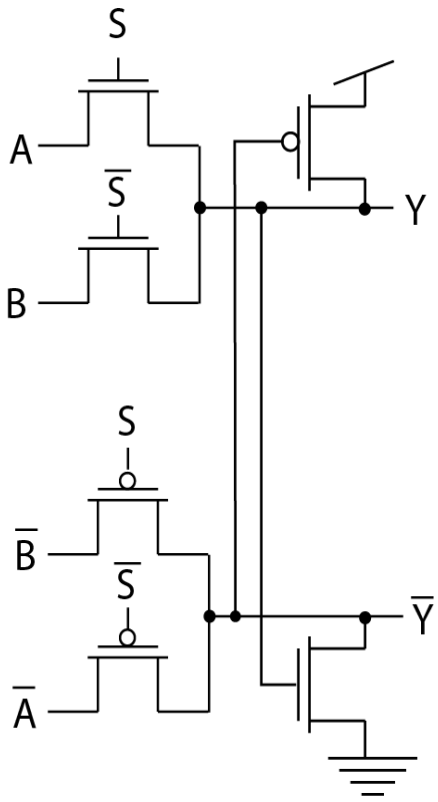


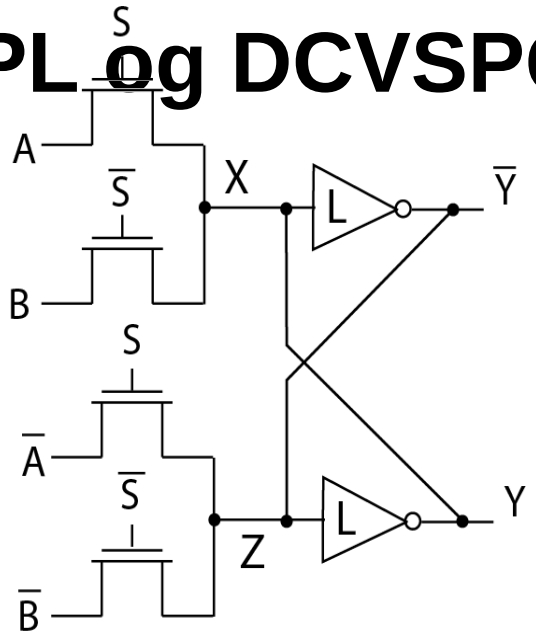
DPL og EEPL

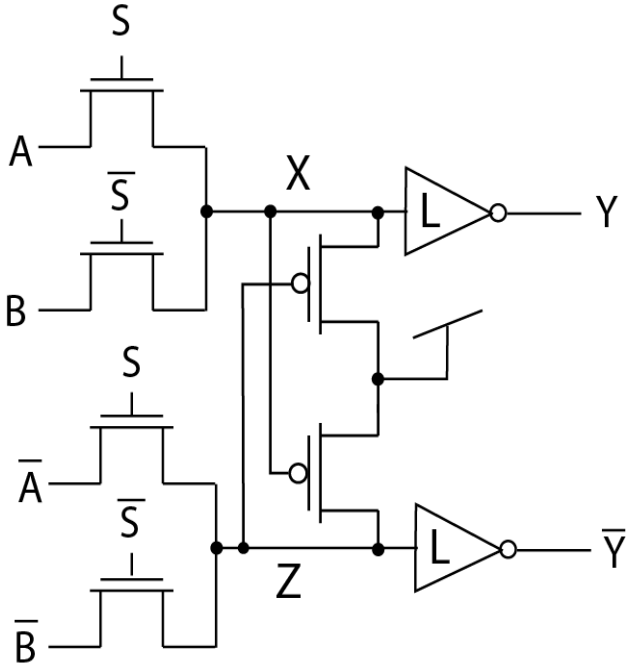




PPL, SRPL og DCVSPG

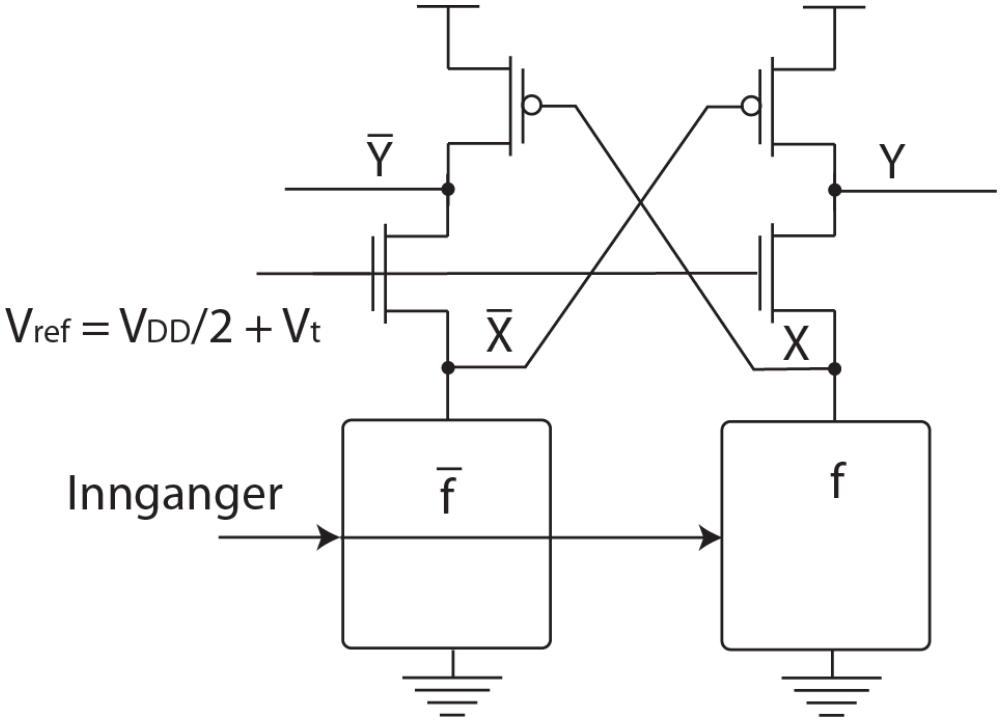




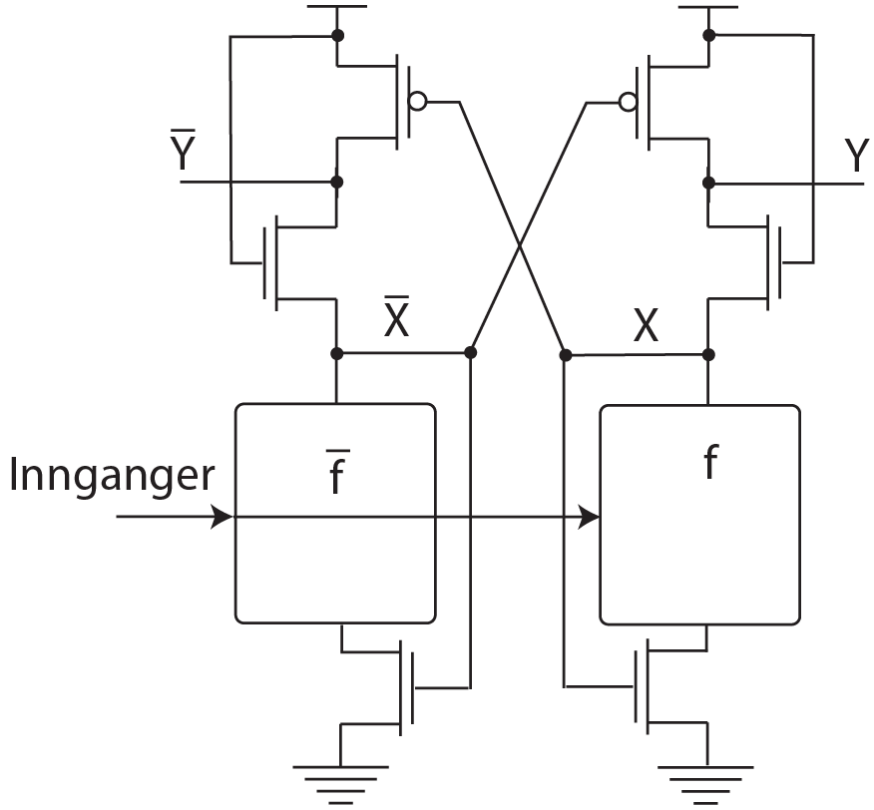


Differensielle kretser

Differensiell split-level:

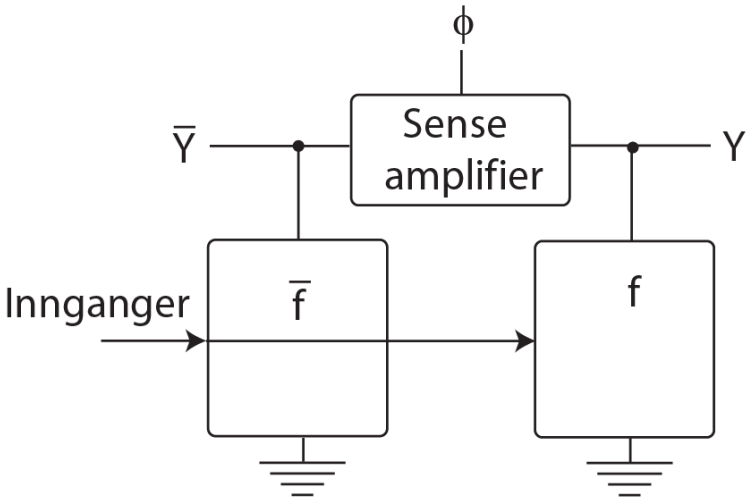


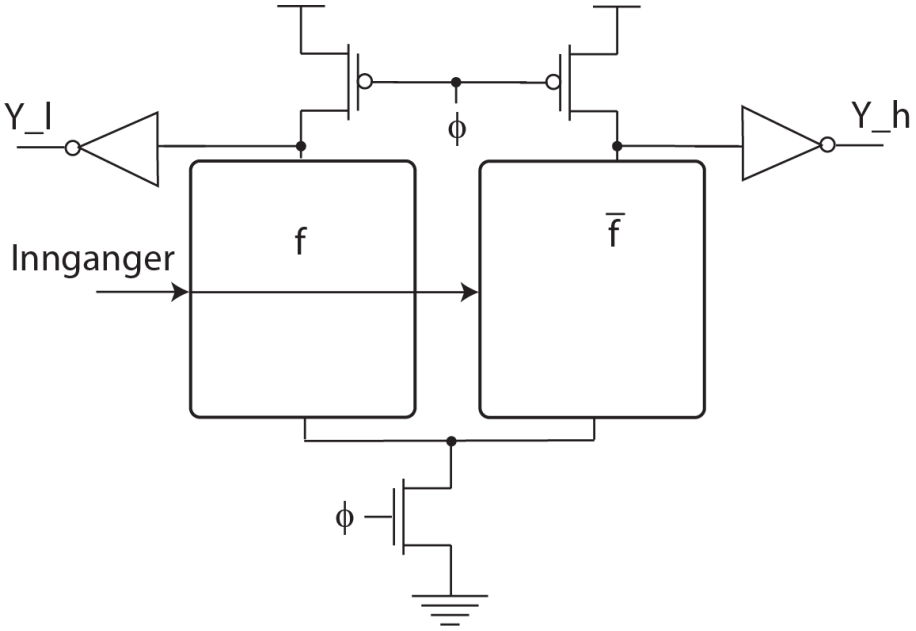
Kaskode nonthreshold logic:

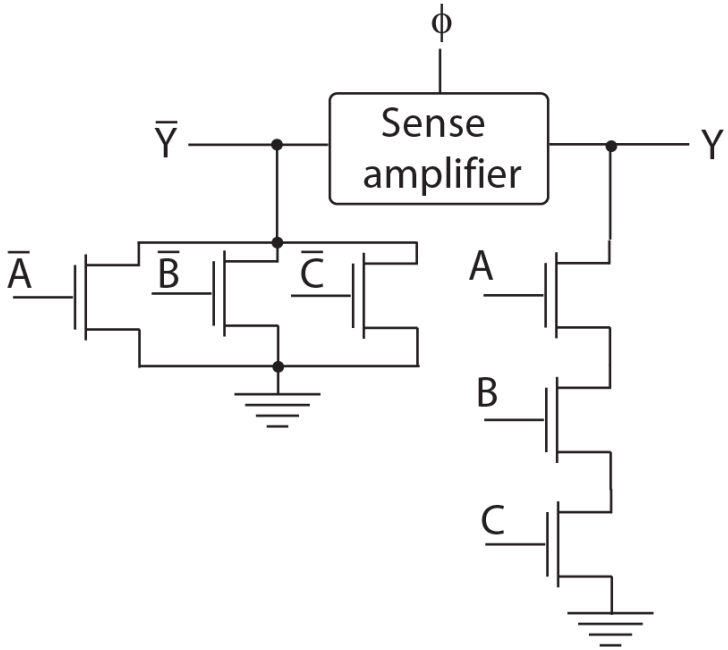


Sense-amplifier kretser

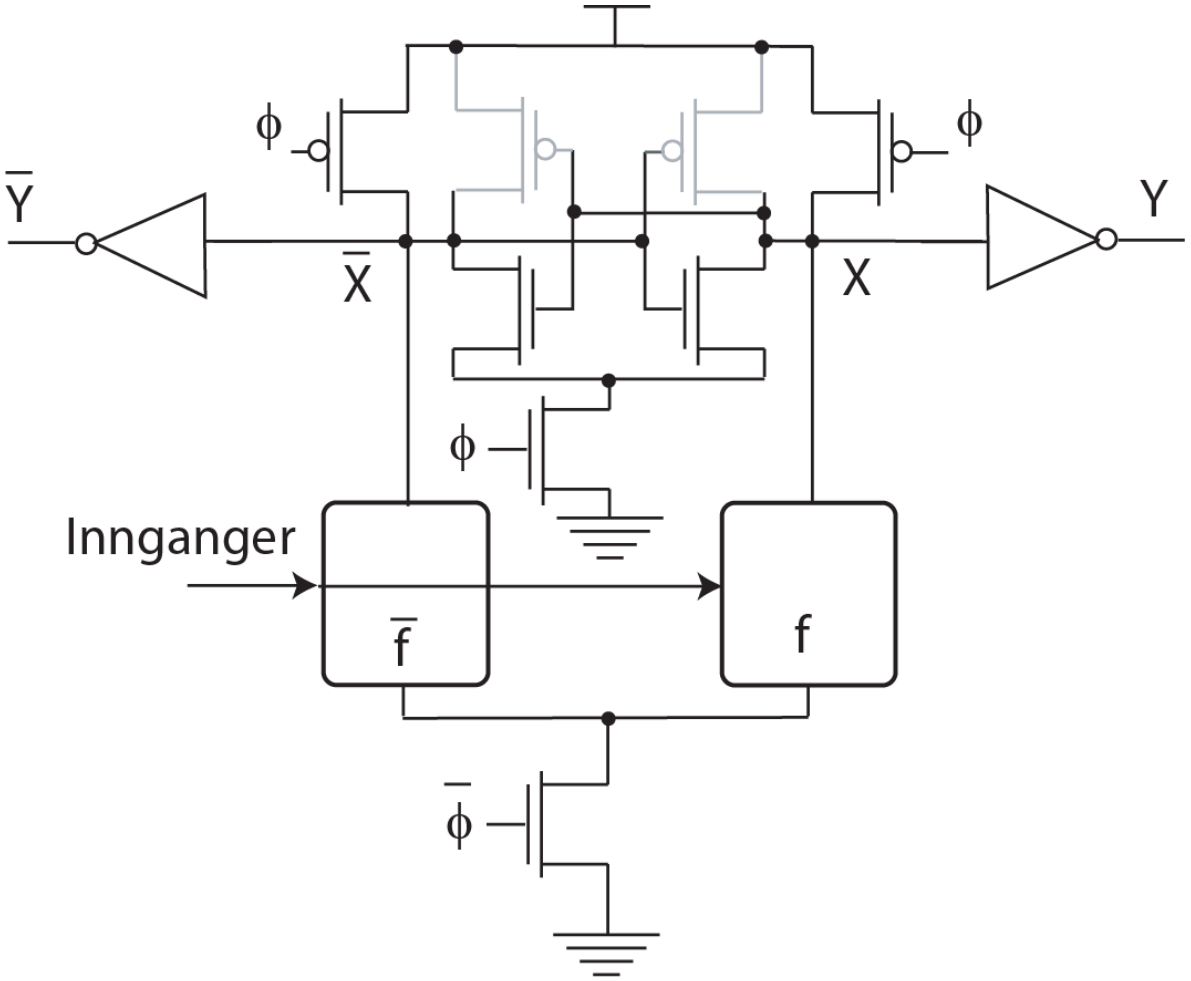
Dual rail domino logikk:



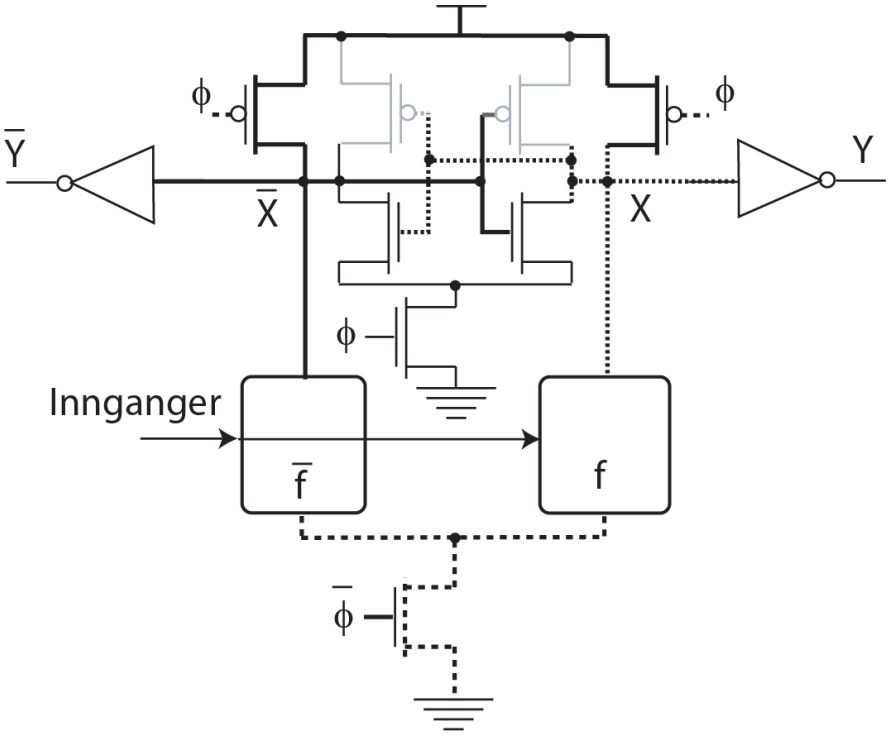




Sample set differensiell logikk



Sample:



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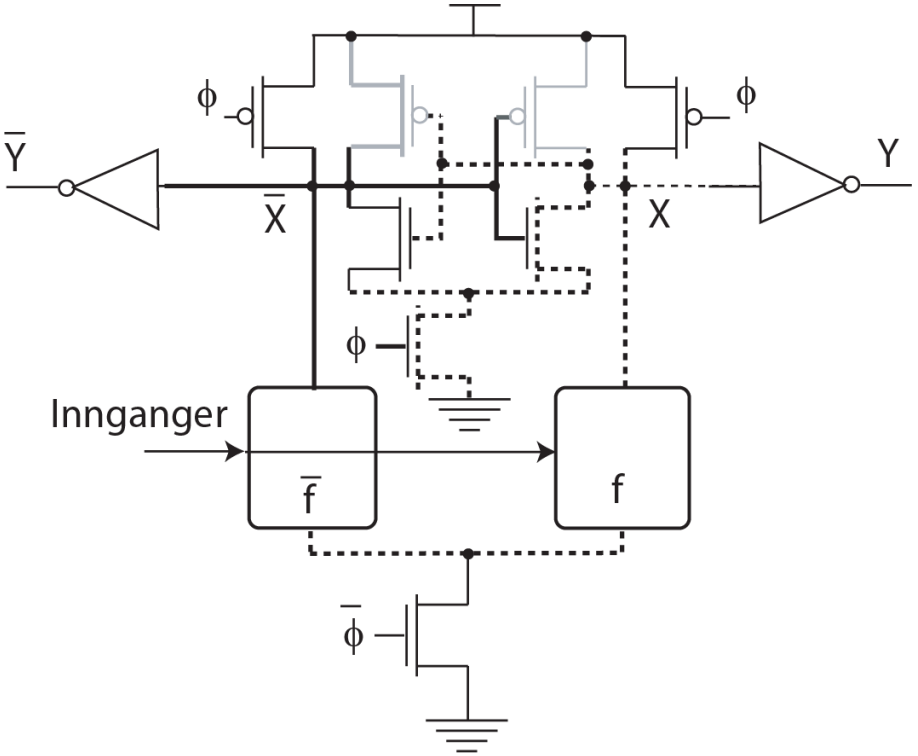
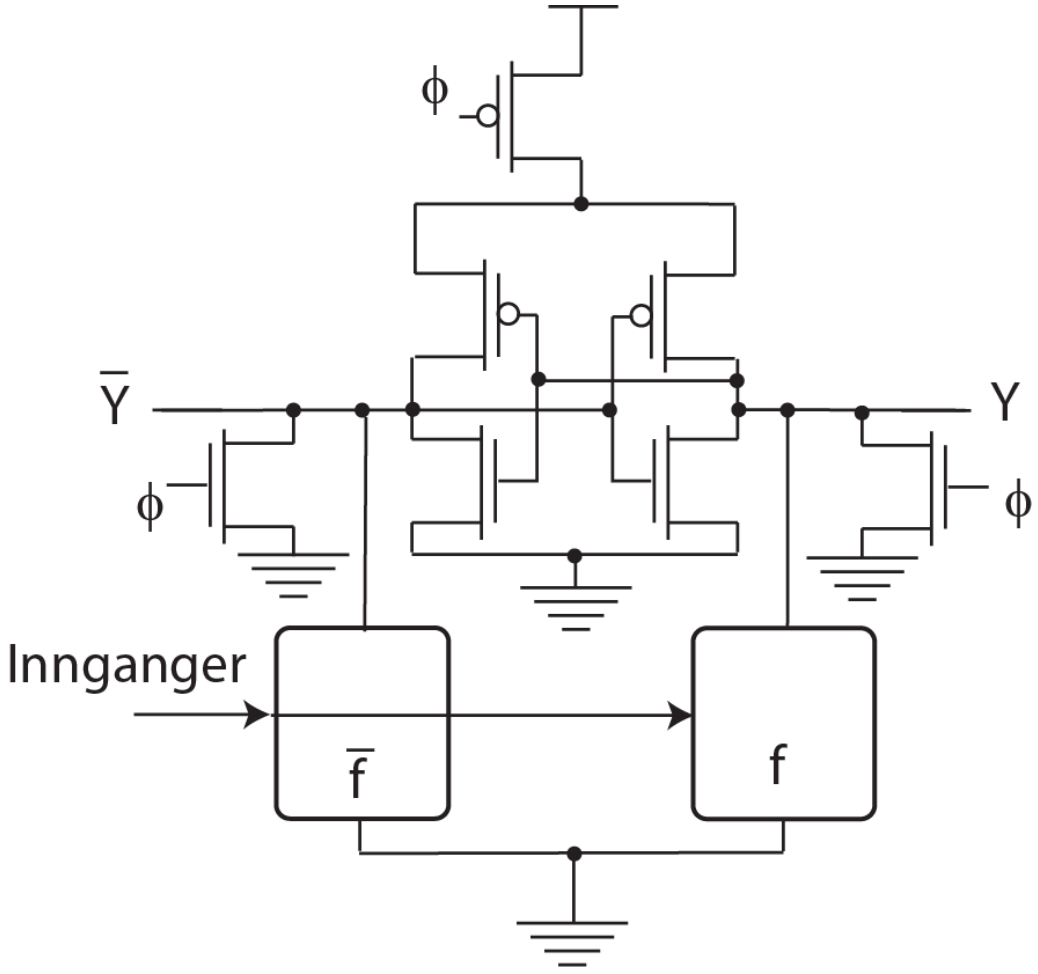


Enable/disable differensiell logikk



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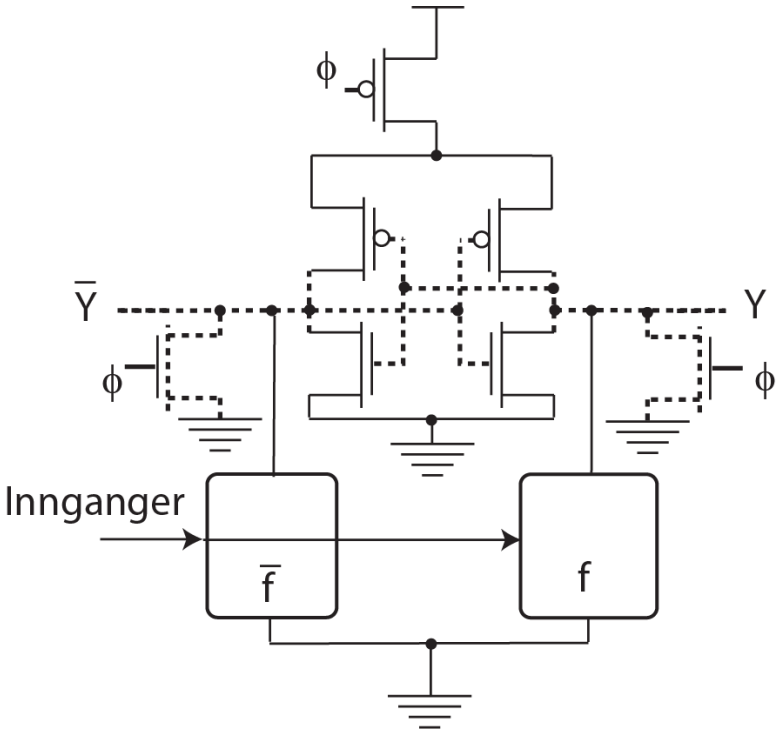
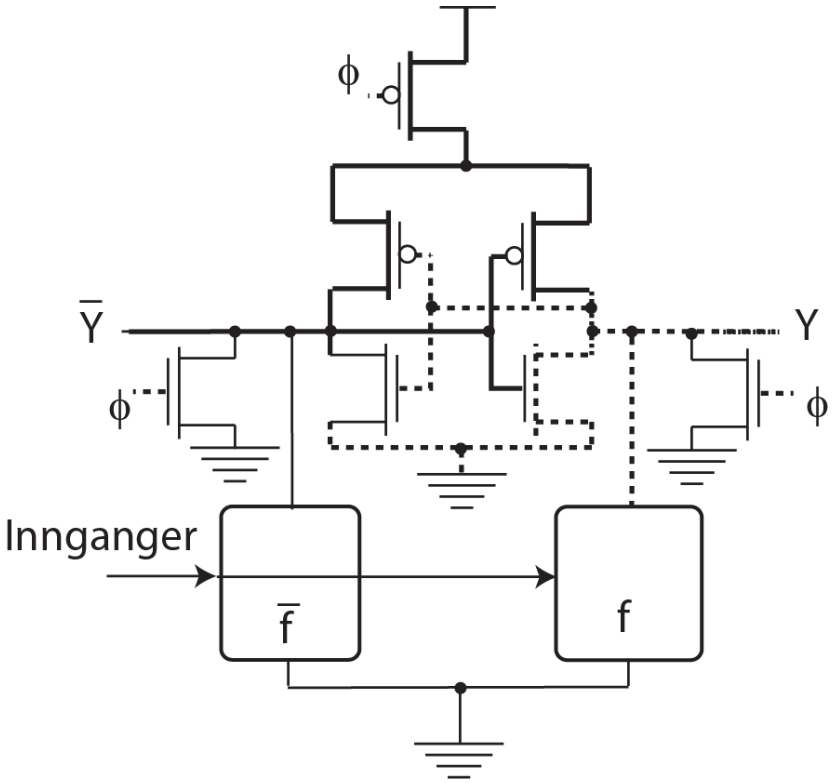
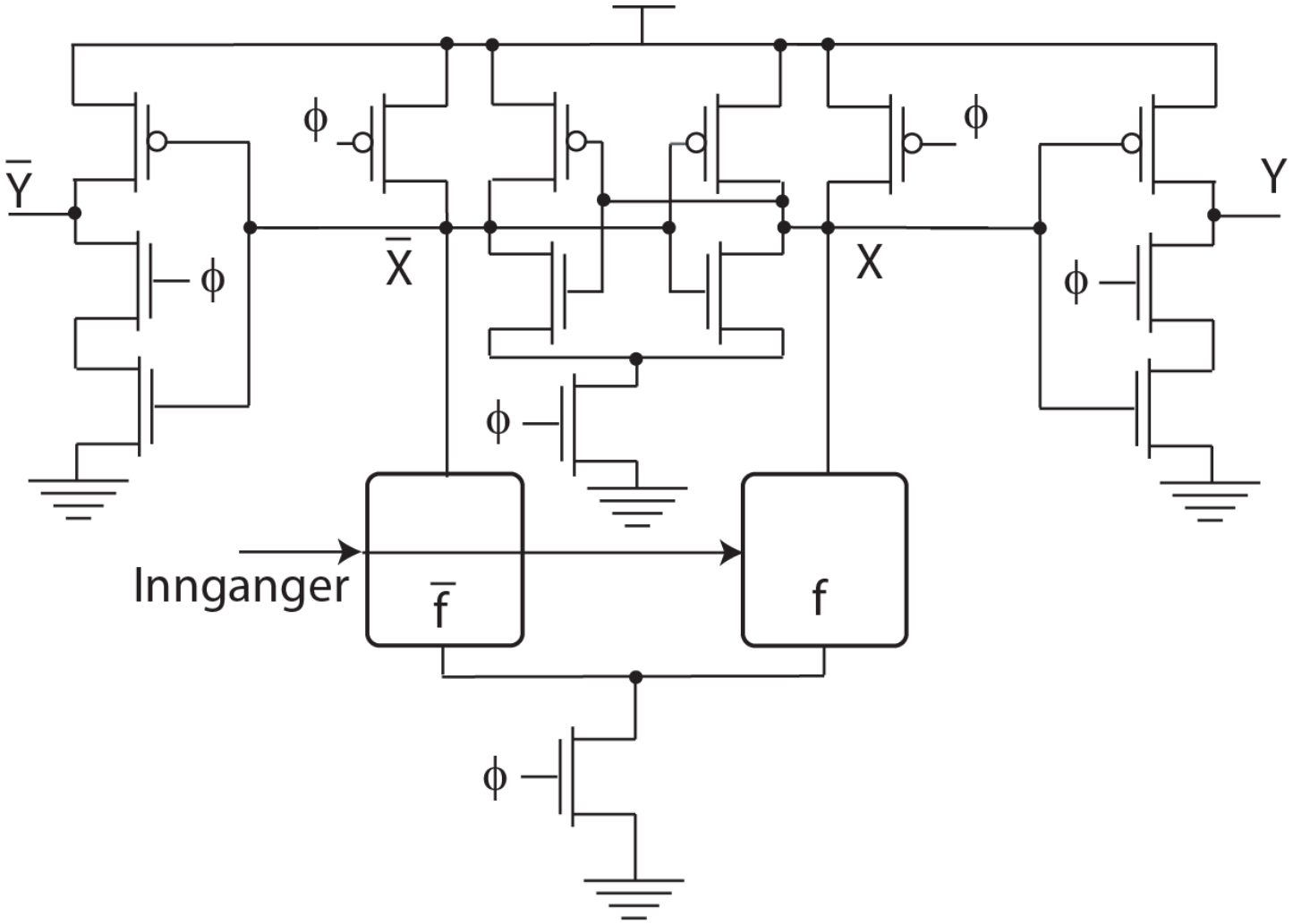


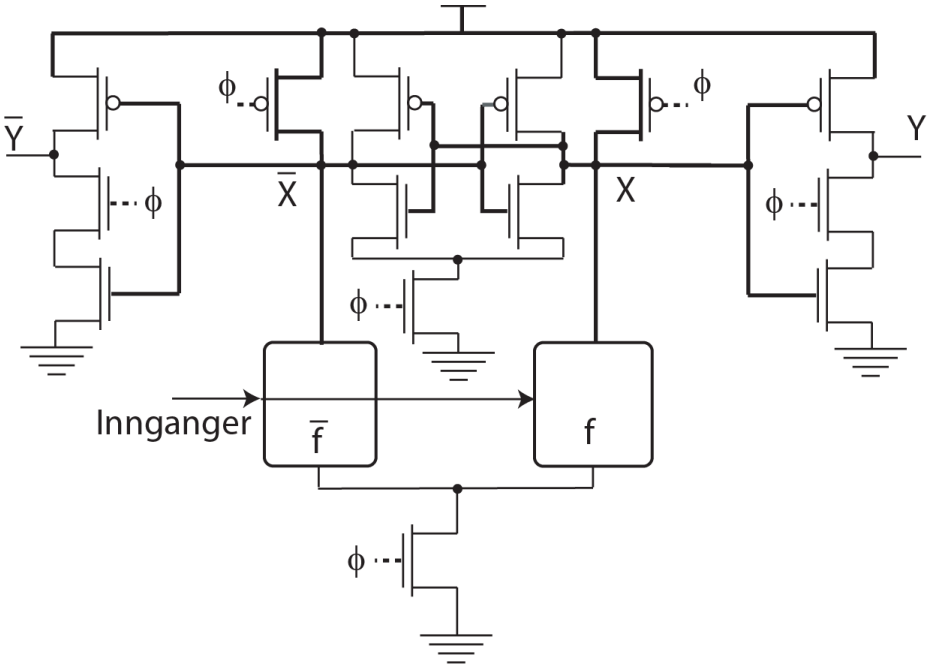
Enable:



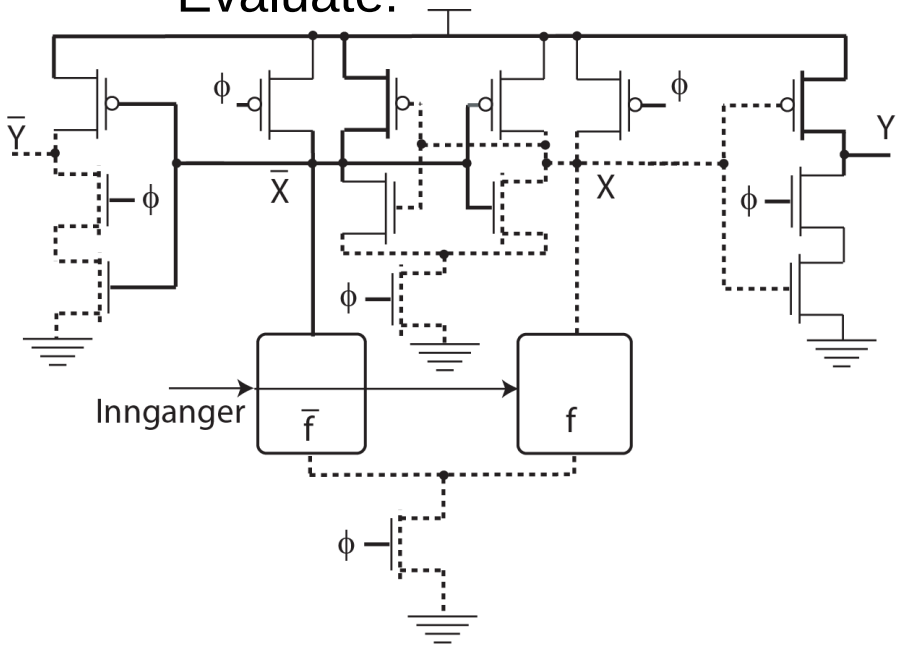
Latched differensiell logikk



Precharge:



Evaluate:



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